

EXHIBIT K



US007383453B2

(12) **United States Patent**
Youngs

(10) **Patent No.:** **US 7,383,453 B2**
(45) **Date of Patent:** ***Jun. 3, 2008**

(54) **CONSERVING POWER BY REDUCING
VOLTAGE SUPPLIED TO AN
INSTRUCTION-PROCESSING PORTION OF
A PROCESSOR**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 55 days.

This patent is subject to a terminal disclaimer.

(21) Appl. No.: **11/213,215**

(22) Filed: **Aug. 25, 2005**

(65) **Prior Publication Data**

US 2005/0283628 A1 Dec. 22, 2005

Related U.S. Application Data

(63) Continuation of application No. 11/103,911, filed on Apr. 11, 2005, now Pat. No. 6,973,585, which is a continuation of application No. 10/135,116, filed on Apr. 29, 2002, now Pat. No. 6,920,574.

(51) **Int. Cl.**
G06F 1/00 (2006.01)
G06F 1/26 (2006.01)

(52) **U.S. Cl.** 713/300; 713/320; 713/324

(58) **Field of Classification Search** 713/300
See application file for complete search history.

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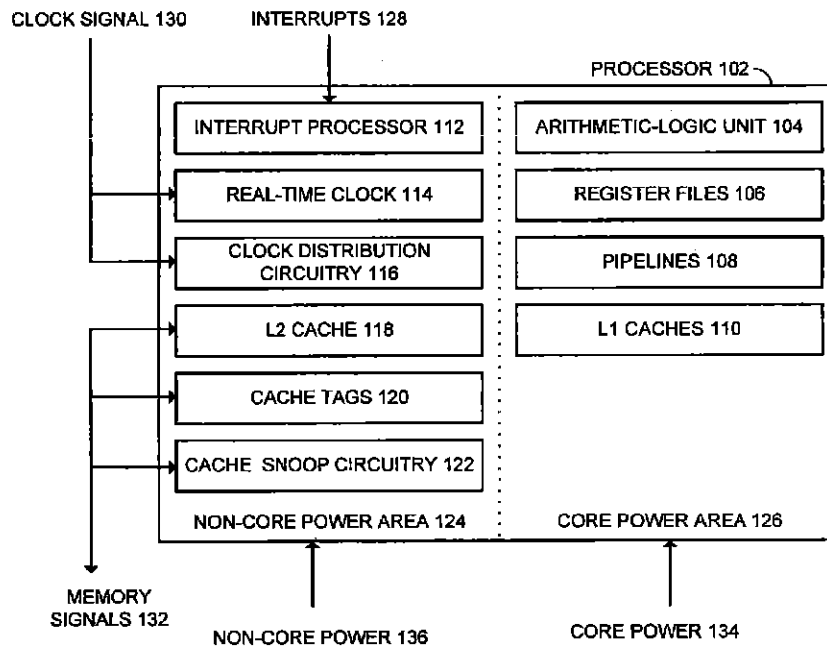
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(57) **ABSTRACT**

One embodiment of the present invention provides a system that facilitates reducing static power consumption of a processor. During operation, the system receives a signal indicating that instruction execution within the processor is to be temporarily halted. In response to this signal, the system halts an instruction-processing portion of the processor, and reduces the voltage supplied to the instruction-processing portion of the processor. Full voltage is maintained to a remaining portion of the processor, so that the remaining portion of the processor can continue to operate while the instruction-processing portion of the processor is in reduced power mode.

21 Claims, 3 Drawing Sheets



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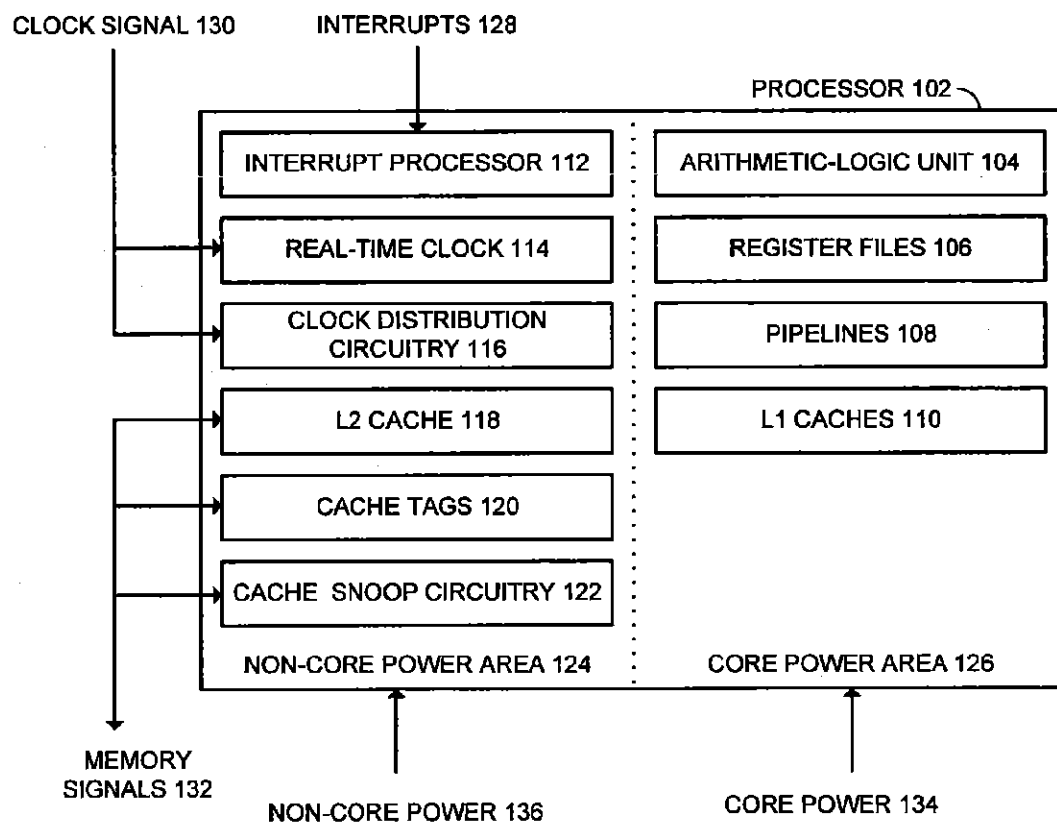


FIG. 1A

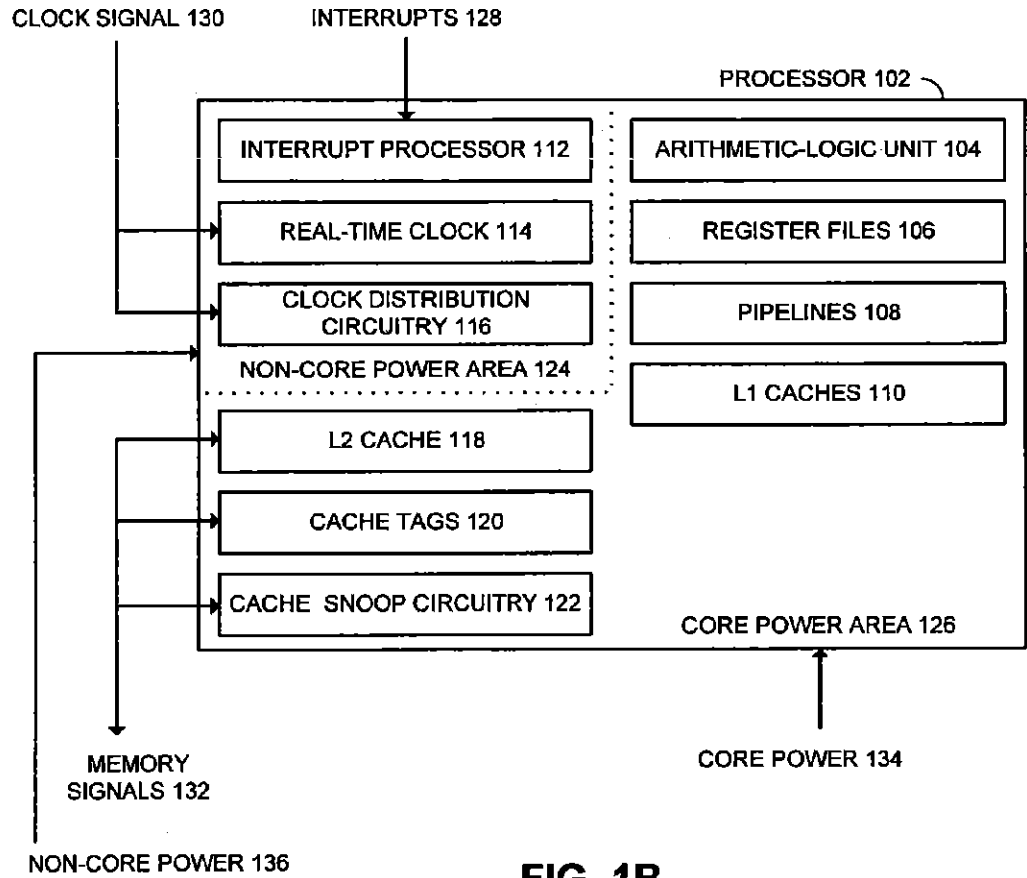


FIG. 1B

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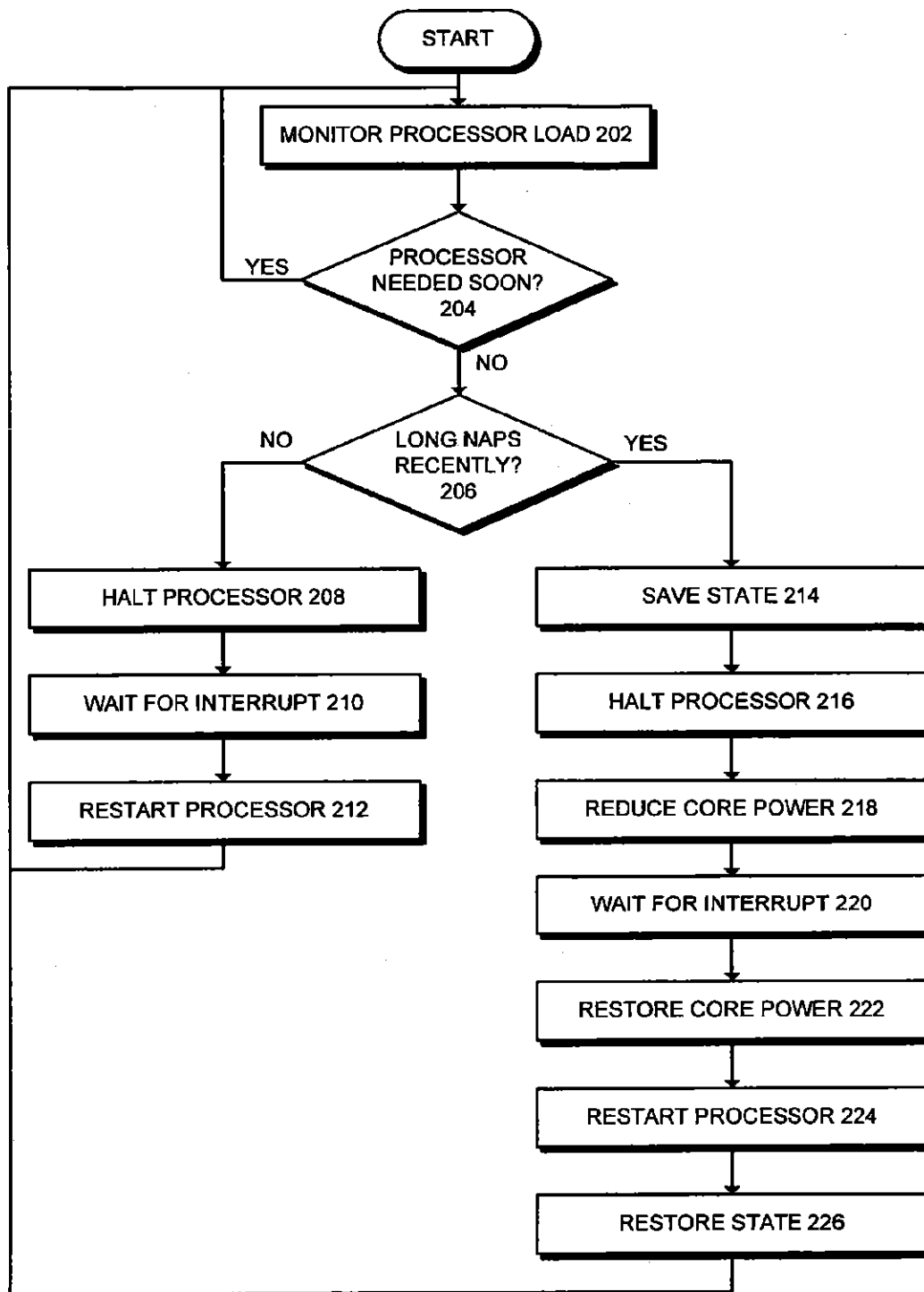


FIG. 2

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CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR

RELATED APPLICATION

This application is a continuation of U.S. patent application Ser. No. 11/103,911, filed 11 Apr. 2005 now U.S. Pat. No. 6,973,585. This application hereby claims priority under 35 U.S.C. §120 to the above-listed application. Note that pending U.S. patent application Ser. No. 11/103,911 is itself a continuation of U.S. patent application Ser. No. 10/135,116, filed 29 Apr. 2002 now U.S. Pat. No. 6,920,574.

BACKGROUND

1. Field of the Invention

The present invention relates to techniques for conserving power usage in computer systems. More specifically, the present invention relates to a method and an apparatus for reducing power consumption in a processor by reducing voltage supplied to an instruction-processing portion of the processor, while maintaining voltage to other portions of the processor.

2. Related Art

Dramatic advances in integrated circuit technology have led to corresponding increases in processor clock speeds. Unfortunately, these increases in processor clock speeds have been accompanied by increased power consumption. Increased power consumption is undesirable, particularly in battery-operated devices such as laptop computers, for which there exists a limited supply of power. Any increase in power consumption decreases the battery life of the computing device.

Modern processors are typically fabricated using Complementary Metal Oxide Semiconductor (CMOS) circuits. CMOS circuits typically consume more power while the circuits are switching, and less power while the circuits are idle. Designers have taken advantage of this fact by reducing the frequency of (or halting) clock signals to certain portions of a processor when the processor is idle. Note that some portions of the processor must remain active, however. For example, a cache memory with its associated snoop circuitry will typically remain active as well as interrupt circuitry and real-time clock circuitry.

Although reducing the frequency of (or halting) a system clock signal can reduce the dynamic power consumption of a processor, static power consumption is not significantly affected. This static power consumption is primarily caused by leakage currents through the CMOS devices. As integration densities of integrated circuits continue to increase, circuit devices are becoming progressively smaller. This tends to increase leakage currents, and thereby increases static power consumption. This increased static power consumption results in reduced battery life, and increases cooling system requirements for battery operated computing devices.

What is needed is a method and an apparatus that reduces static power consumption for a processor in a battery operated computing device.

SUMMARY

One embodiment of the present invention provides a system that facilitates reducing static power consumption of a processor. During operation, the system receives a signal

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indicating that instruction execution within the processor is to be temporarily halted. In response to this signal, the system halts an instruction-processing portion of the processor, and reduces the voltage supplied to the instruction-processing portion of the processor. Full voltage is maintained to a remaining portion of the processor, so that the remaining portion of the processor can continue to operate while the instruction-processing portion of the processor is in reduced power mode.

In one embodiment of the present invention, reducing the voltage supplied to the instruction-processing portion of the processor involves reducing the voltage to a minimum value that maintains state information within the instruction-processing portion of the processor.

In one embodiment of the present invention, reducing the voltage supplied to the instruction-processing portion of the processor involves reducing the voltage to zero.

In one embodiment of the present invention, the system saves state information from the instruction-processing portion of the processor prior to reducing the voltage supplied to the instruction-processing portion of the processor. This state information can either be saved in the remaining portion of the processor or to the main memory of the computer system.

In one embodiment of the present invention, upon receiving a wakeup signal, the system: restores full voltage to the instruction-processing portion of the processor; restores state information to the instruction-processing portion of the processor; and resumes processing of computer instructions.

In one embodiment of the present invention, maintaining full voltage to the remaining portion of the processor involves maintaining full voltage to a snoop-logic portion of the processor, so that the processor can continue to perform cache snooping operations while the instruction-processing portion of the processor is in the reduced power mode.

In one embodiment of the present invention, the system also reduces the voltage to a cache memory portion of the processor. In this embodiment, the system writes cache memory data to main memory prior to reducing the voltage.

In one embodiment of the present invention, the remaining portion of the processor includes a control portion of the processor containing interrupt circuitry and clock circuitry.

In one embodiment of the present invention, the remaining portion of the processor includes a cache memory portion of the processor.

BRIEF DESCRIPTION OF THE FIGURES

FIG. 1A illustrates different power areas within processor 102 in accordance with an embodiment of the present invention.

FIG. 1B illustrates alternate power areas within processor 102 in accordance with an embodiment of the present invention.

FIG. 2 is a flowchart illustrating the process of monitoring processor load and switching to power saving modes in accordance with an embodiment of the present invention.

DETAILED DESCRIPTION

The following description is presented to enable any person skilled in the art to make and use the invention, and is provided in the context of a particular application and its requirements. Various modifications to the disclosed embodiments will be readily apparent to those skilled in the art, and the general principles defined herein may be applied to other embodiments and applications without departing

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from the spirit and scope of the present invention. Thus, the present invention is not intended to be limited to the embodiments shown, but is to be accorded the widest scope consistent with the principles and features disclosed herein.

Processor 102

FIG. 1A illustrates different power areas within processor 102 in accordance with an embodiment of the present invention. Processor 102 is divided into a core power area 126, and a non-core power area 124. Core power area 126 includes the instruction-processing portion of processor 102. Specifically, core power area 126 includes arithmetic-logic unit 104, register files 106, pipelines 108, and possibly level one (L1) caches 110. Note that L1 caches 110 can alternatively be located in non-core power area 124.

Arithmetic-logic unit 104 provides computational and logical operations for processor 102. Register files 106 provide source operands, intermediate storage, and destination locations for instructions being executed by arithmetic-logic unit 104. Pipelines 108 provides a steady stream of instructions to arithmetic-logic unit 104. Instructions in pipelines 108 are decoded in transit. Therefore, pipelines 108 may contain instructions in various stages of decoding and execution. L1 caches 110 include data caches and instruction caches for arithmetic-logic unit 104. L1 caches 110 are comprised of very high-speed memory to provide fast access for instructions and data. In one embodiment of the present invention, L1 caches 110 includes a write-through data cache.

Non-core power area 124 comprises the remaining portion of processor 102 and includes interrupt processor 112, real-time clock 114, clock distribution circuitry 116, level two (L2) caches 118, cache tags 120, and cache snoop circuitry 122. In general, non-core power area 124 includes portions of processor 102 that are not directly involved in processing instructions, and that need to operate while instruction processing is halted.

Interrupt processor 112 monitors interrupts 128 and periodically interrupts the execution of applications to provide services to external devices requiring immediate attention. Interrupt processor 112 can also provide a wake-up signal to core power area 126 as described below. Real-time clock 114 provides time-of-day services to processor 102. Typically, real-time clock 114 is set upon startup from a battery operated real-time clock in the computer and thereafter provides time to the system. Clock distribution circuitry 116 provides clock signals for processor 102. Distribution of these clock signals can be switched off or reduced for various parts of processor 102. For example, clock distribution to core power area 126 can be stopped while the clock signals to non-core power area 124 continue. The acts of starting and stopping of these clock signals are known in the art and will not be described further. Real-time clock 114 and clock distribution circuitry 116 receive clock signal 130 from the computer system. Clock signal 130 is the master clock signal for the system.

L2 cache 118 provides a second level cache for processor 102. Typically, an L2 cache is larger and slower than an L1 cache, but still provides faster access to instructions and data than can be provided by main memory. Cache tags 120 provide an index into data stored in L2 cache 118. Cache snoop circuitry 122 invalidates cache lines base primarily on other processors accessing their own cache lines, or I/O devices doing memory transfers, even when instruction processing has been halted. L2 cache 118, cache tags 120, and cache snoop circuitry 122 communicate with the computer system through memory signals 132.

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Non-core power area 124 receives non-core power 136 and core power area 126 receives core power 134. The voltage applied for non-core power 136 remains at a voltage that allows circuitry within non-core power area 124 to remain fully active at all times. In contrast, non-core power 136 may provide different voltages to non-core power area 124 based upon the operating mode of processor 102. For example, if processor 102 is a laptop attached to external electrical power, the voltage provided to non-core power 136 (and to core power 134 during instruction processing) may be higher than the minimum voltage, thus providing faster execution of programs.

The voltage applied to core power 134 remains sufficiently high during instruction processing so that core power area 126 remains fully active. However, when processor 102 receives a signal that processing can be suspended, the voltage supplied by core power 134 can be reduced.

In one embodiment of the present invention, the voltage in core power 134 is reduced to the minimum value that will maintain state information within core power area 126, but this voltage is not sufficient to allow processing to continue. In another embodiment of the present invention, the voltage at core power 134 is reduced to zero. In this embodiment, the state of core power area 126 is first saved before the voltage is reduced to zero. This state can be saved in a dedicated portion of L2 cache 118, in main memory, or in another dedicated storage area. Upon receiving an interrupt or other signal indicating that processing is to resume, the voltage in core power 134 is restored to a normal level, saved state is restored, and processing is restarted.

FIG. 1B illustrates an alternative partitioning of power areas within processor 102 in accordance with an embodiment of the present invention. As shown in FIG. 1B, L2 cache 118, cache tags 120, and cache snoop circuitry 122 are included in core power area 126 rather than in non-core power area 124. In this embodiment, the voltage supplied as core power 134 is reduced or set to zero as described above, however, the cache circuitry within processor 102 is also put into the reduced power mode. Prior to reducing the voltage supplied to core power area 126, data stored in L2 cache 118 is flushed to main memory. Additionally, if the voltage at core power 134 is reduced to zero, the state of processor 102 is first saved in main memory.

Monitoring and Switching

FIG. 2 is a flowchart illustrating the process of monitoring processor load and switching to power saving modes in accordance with an embodiment of the present invention. The system starts by monitoring the processor load (step 202). Next, the system determines if the processor will be needed soon (step 204). This determination is made based on the current execution pattern and the cost of entering and recovering from nap mode. This cost, calculated in power usage, must be less than the power wasted by not going into nap mode. If the processor will be needed soon at step 204, the process returns to step 202 to continue monitoring the processor load.

If the processor will not be needed soon at step 204, the system determines if the processor has been taking long naps recently (step 206). If not, the system enters a normal nap mode, which involves halting the processor without reducing any voltages (step 208). Typically, halting the processor involves removing the clock signals to the core power area of the processor. After halting the processor, the system waits for an interrupt (step 210). Upon receiving an interrupt or other signal requiring a restart, the system restarts instruc-

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tion processing (step 212). After restarting instruction processing, the process returns to step 202 to continue monitoring the processor load.

If the processor has recently been taking long naps at step 206, the system enters a deep nap mode, which involves saving the state information from the core power area (step 214), halting the processor (step 216), and then reducing the voltage supplied to the core power area (step 218). After reducing the voltage, the system waits for an interrupt (step 220).

Upon receiving the interrupt or other signal requiring a restart, the system restores the voltage to the core power area (step 222). Next, the modules within the core power area are restarted (step 224). The system then restores the state information that was saved at step 214 (step 226). After the processor has been restarted, the process returns to step 202 to continue monitoring the processor load. Note that the above description applies when the processor is used to save and restore the state information. In cases where dedicated hardware saves and restores the state information, steps 214 and 216, and steps 224 and 226 can be reversed. Note also that if the voltage supplied to the core power area 126 is reduced but maintained at a level where modules in the core power do not lose state information, steps 216 and 224 are not required.

The foregoing descriptions of embodiments of the present invention have been presented for purposes of illustration and description only. They are not intended to be exhaustive or to limit the present invention to the forms disclosed. Accordingly, many modifications and variations will be apparent to practitioners skilled in the art. Additionally, the above disclosure is not intended to limit the present invention. The scope of the present invention is defined by the appended claims.

What is claimed is:

1. An instruction-processing system with minimal static power leakage, the instruction-processing system comprising:

a core with instruction-processing circuitry;
an area coupled to the core;
a core voltage provided to the core; and
an area voltage provided to the area;
wherein in a normal operation mode:

a clock signal to the core is active;
the core voltage is a first value;
the core is active;
the area voltage is a second value; and
the area is active;

wherein in a first power-saving mode that is exited upon receipt of an interrupt signal:

the clock signal to the core is inactive;
the core voltage is equal to or greater than the first value; and
the area voltage is equal to or greater than the second value;

wherein in a second power-saving mode that can be exited upon receipt of a signal that is not an interrupt signal:
the clock signal to the core is inactive;
the core voltage is less than the first value; and
the area voltage is equal to or greater than the second value.

2. The instruction-processing system of claim 1, wherein the first power-saving mode can be exited upon receipt of a signal that is not an interrupt signal.

3. The instruction-processing system of claim 1, wherein the area comprises a cache.

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4. The instruction-processing system of claim 3, wherein the area further comprises cache tags.

5. The instruction-processing system of claim 1, wherein prior to entering the second power-saving mode, the state of the core is saved to a memory.

6. The instruction-processing system of claim 1, wherein upon exiting the second power-saving mode, the state of the core is restored.

7. The instruction-processing system of claim 1, wherein in the second power-saving mode, the core voltage is at zero.

8. A method for minimizing static power leakage in an instruction-processing system, wherein the instruction-processing system comprises a core with instruction-processing circuitry, an area coupled to the core, a core voltage provided to the core, and an area voltage provided to the area, the method comprising:

entering a normal operation mode by:

providing a clock signal to the core;
providing the core with a core voltage that is equal to a first value;
providing the area with an area voltage that is equal to a second value;

entering a first power-saving mode by:

disabling the clock signal to the core;
providing the core with a core voltage that is equal to or greater than the first value; and
providing the area with an area voltage that is equal to or greater than the second value;

exiting the first power-saving mode upon receipt of an interrupt signal;

entering a second power-saving mode by:

disabling the clock signal to the core;
setting the core voltage to a value less than the first value; and

providing the area with an area voltage that is equal to or greater than the second value; and

exiting the second power-saving mode upon receipt of a signal that is not an interrupt signal.

9. The method of claim 8, further comprising exiting the first power-saving mode upon receipt of a signal that is not an interrupt signal.

10. The instruction-processing system of claim 8, wherein the area comprises a cache.

11. The method of claim 10, wherein the area further comprises cache tags.

12. The method of claim 8, further comprising saving the state of the core to a memory prior to entering the second power-saving mode.

13. The method of claim 8, further comprising restoring the state of the core upon exiting the second power-saving mode.

14. The method of claim 8, wherein in the second power-saving mode, setting the core voltage to the value less than the first value comprises setting the core voltage to zero.

15. A computer-readable medium containing data representing an instruction-processing system with minimal static power leakage, the instruction-processing system comprising:

a core with instruction-processing circuitry;
an area coupled to the core;
a core voltage provided to the core; and
an area voltage provided to the area;
wherein in a normal operation mode:
a clock signal to the core is active;
the core voltage is a first value;
the core is active;

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the area voltage is a second value, and
the area is active;
wherein in a first power-saving mode that is exited upon
receipt of an interrupt signal:
the clock signal to the core is inactive;
the core voltage is equal to or greater than the first
value; and
the area voltage is equal to or greater than the second
value;
wherein in a second power-saving mode that can be exited 10
upon receipt of a signal that is not an interrupt signal:
the clock signal to the core is inactive;
the core voltage is less than the first value; and
the area voltage is equal to or greater than the second
value.
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16. The computer-readable medium of claim 15, wherein
the first power-saving mode can be exited upon receipt of a
signal that is not an interrupt signal.

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17. The computer-readable medium of claim 15, wherein
the area comprises a cache.
18. The computer-readable medium of claim 17, wherein
the area further comprises cache tags.
19. The computer-readable medium of claim 15, wherein
prior to entering the second power-saving mode, the state of
the core is saved to a memory.
20. The computer-readable medium of claim 15, wherein
upon exiting the second power-saving mode, the state of the
core is restored.
21. The computer-readable medium stem of claim 15,
wherein in the second power-saving mode, the core voltage
is at zero.

* * * * *

EXHIBIT L



US005848105A

United States Patent [19]
Gardner et al.

[11] **Patent Number:** **5,848,105**
 [45] **Date of Patent:** **Dec. 8, 1998**

[54] **GMSK SIGNAL PROCESSORS FOR IMPROVED COMMUNICATIONS CAPACITY AND QUALITY**

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[21] **Appl. No.:** **729,625**

[22] **Filed:** **Oct. 10, 1996**

[51] **Int. Cl.⁶** **H03D 3/00**

[52] **U.S. Cl.** **375/336; 375/350; 375/346;**
364/572; 364/574; 370/478

[58] **Field of Search** **375/854, 274,**
375/278, 334, 336, 346, 350; 364/572,
574, 581; 370/464, 465, 478

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Primary Examiner—Jason Chan

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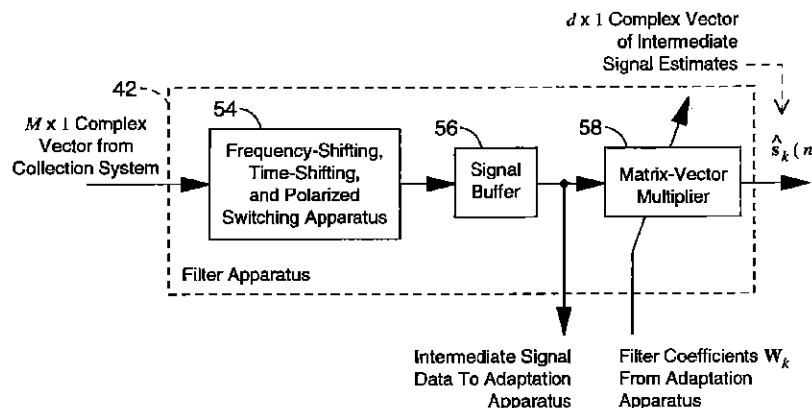
Attorney, Agent, or Firm—John P. O'Banion

[57]

ABSTRACT

A method and apparatus for separating and removing distortion from interfering co-channel signals and suppressing adjacent-channel interfering signals of the Gaussian Minimum-Shift Keyed (GMSK) or other MSK type with filtering structures that exploit the cyclostationarity of the received GMSK or other MSK signals in order to accommodate a greater number (or the same number, but with greater quality) of transmitted signals received by one or more antennas than can be accommodated by existing filters. The parameters in these filtering structures are adapted by either of two adaptation apparatus that exploit both the known training sequence that is transmitted in most wireless communications systems, and the constant modulus property exhibited by each of the transmitted GMSK or other MSK signals.

12 Claims, 31 Drawing Sheets



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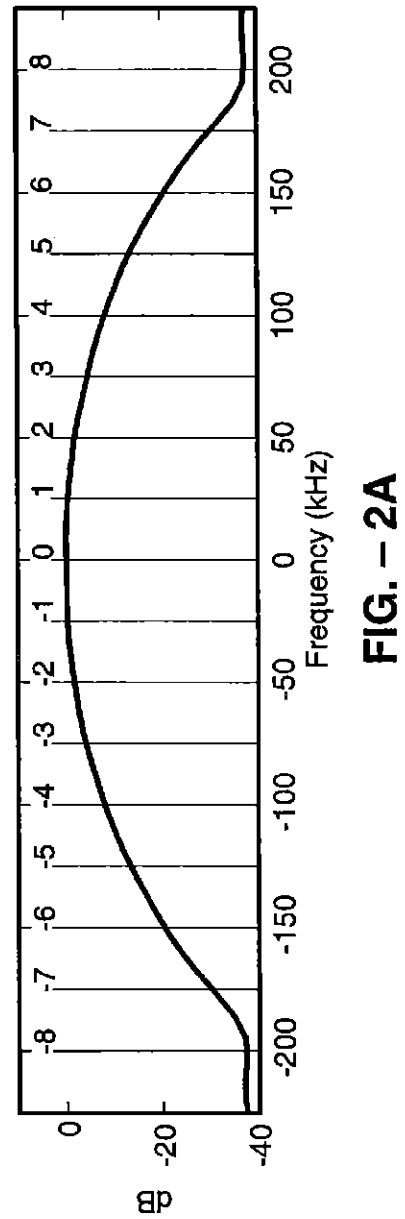
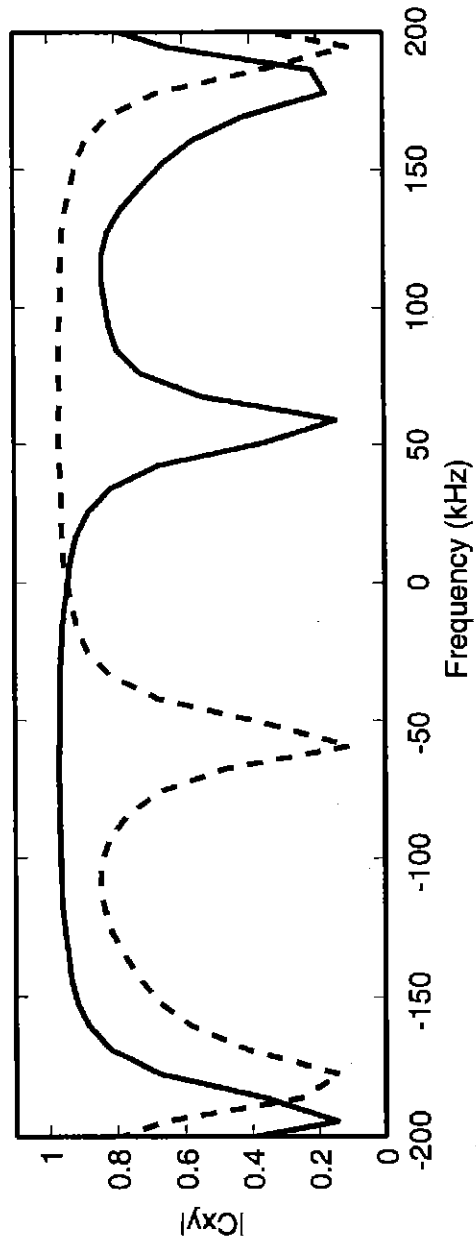
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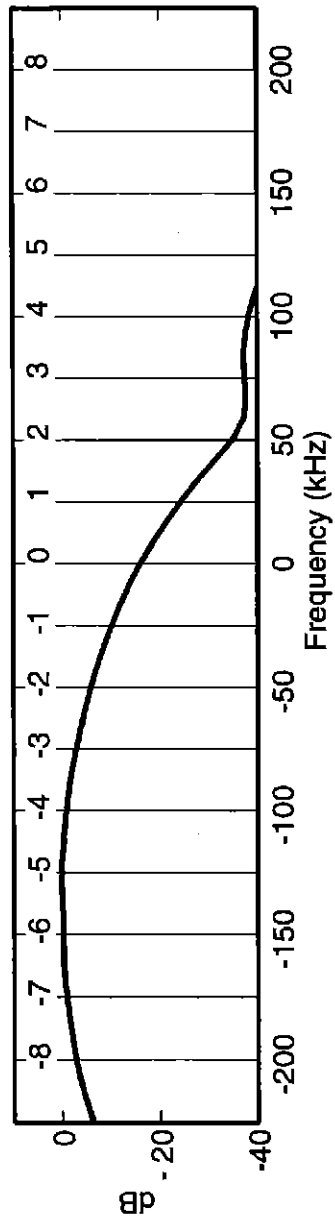


FIG. - 2B

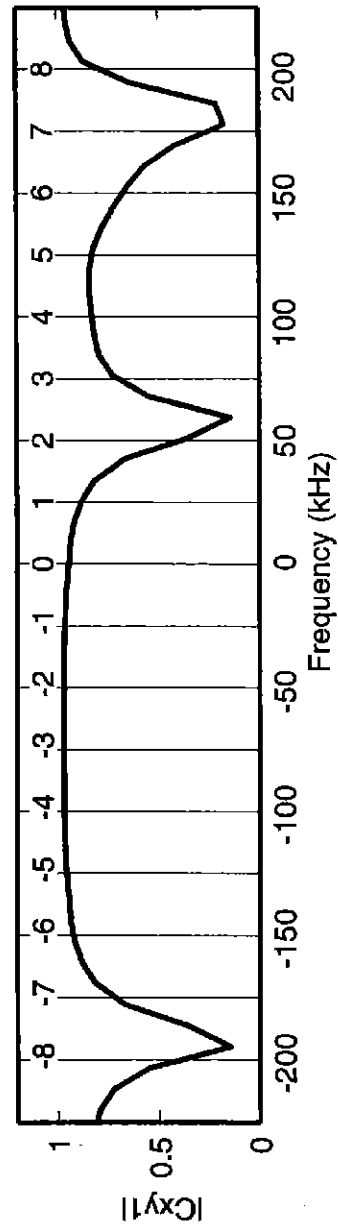


FIG. - 2C

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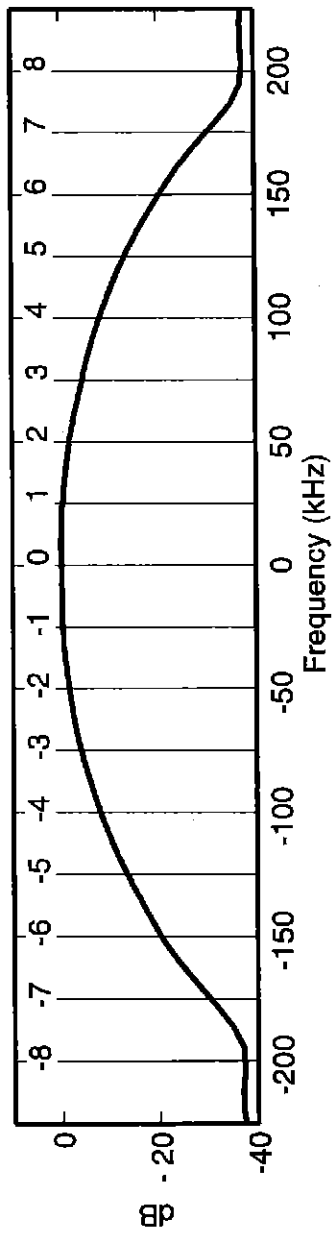


FIG. - 3A

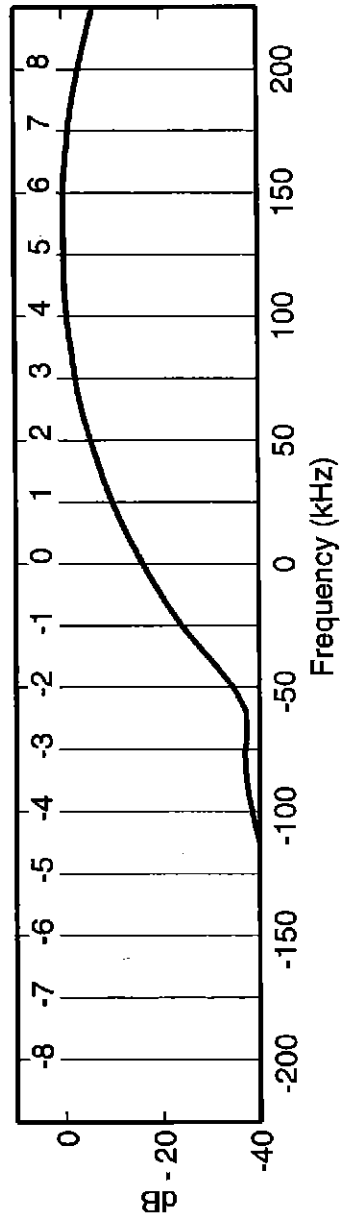


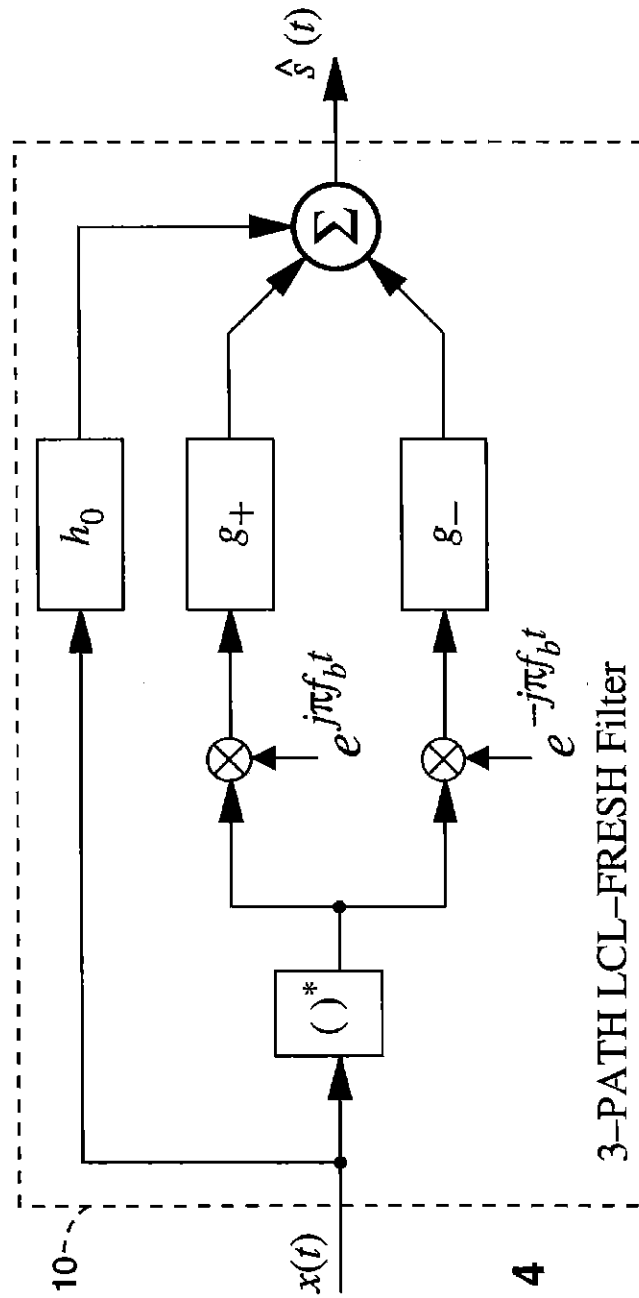
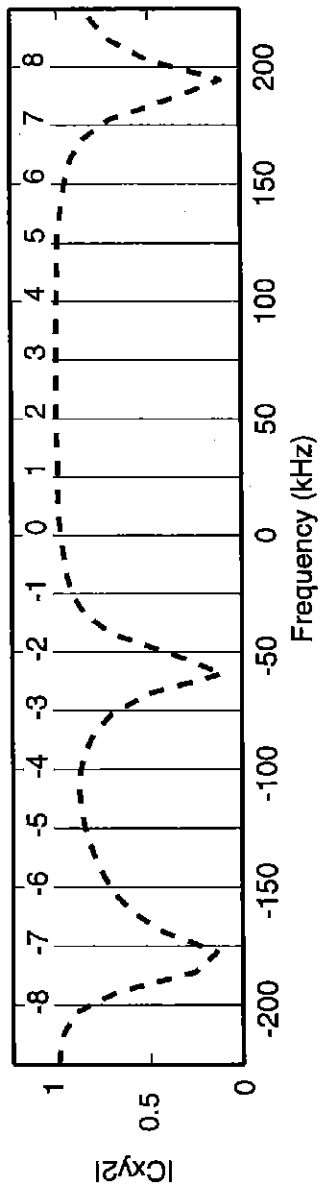
FIG. - 3B

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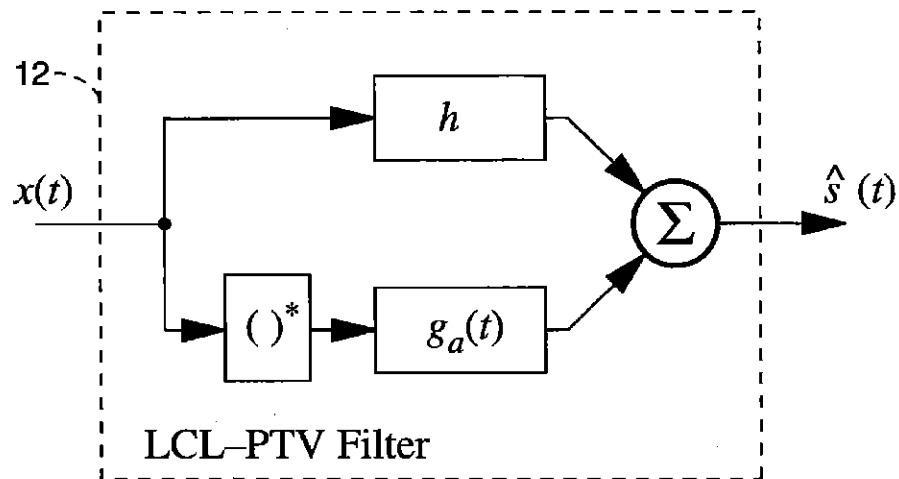


FIG. - 5

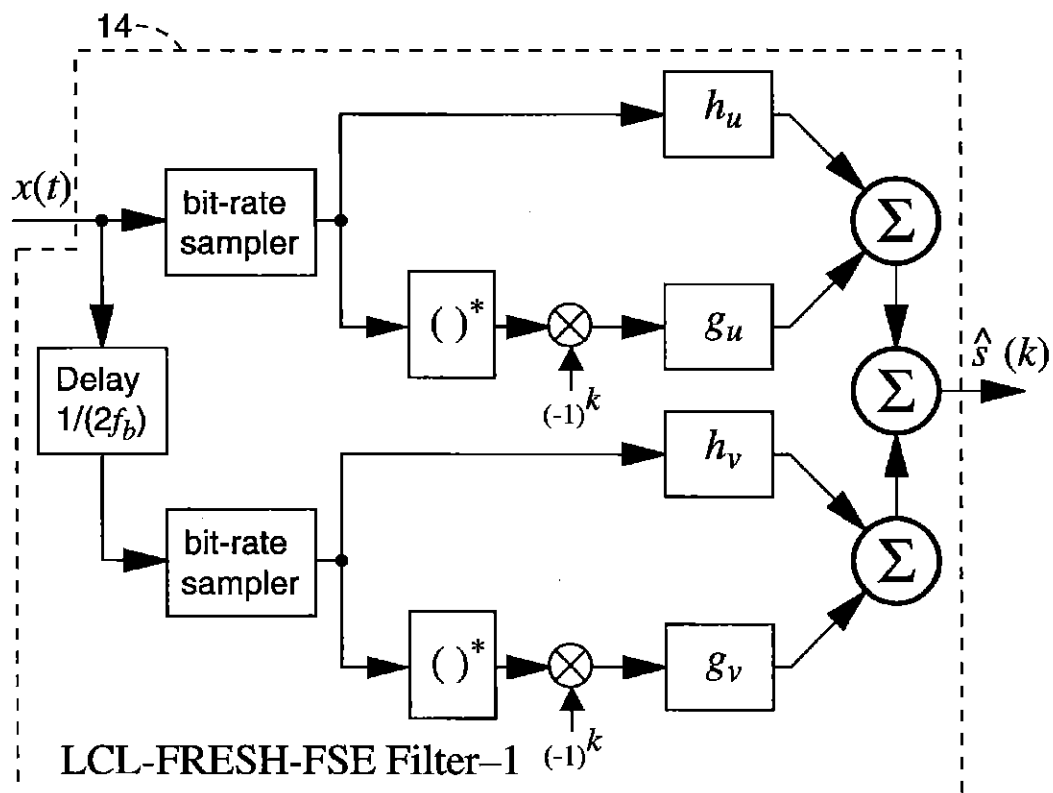


FIG. - 6

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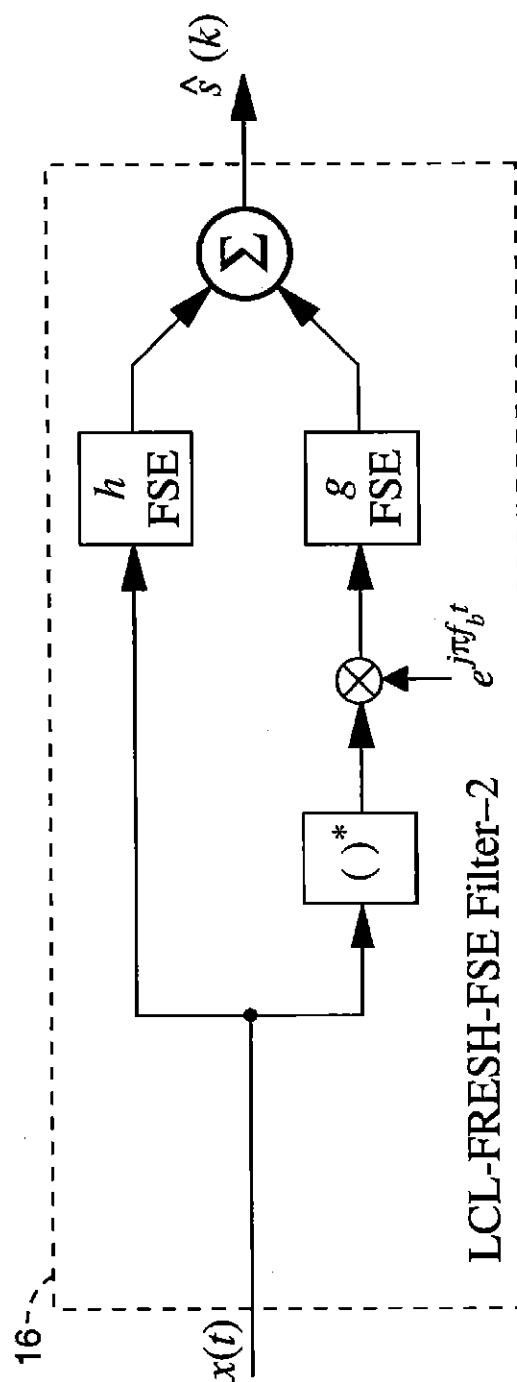


FIG. - 7

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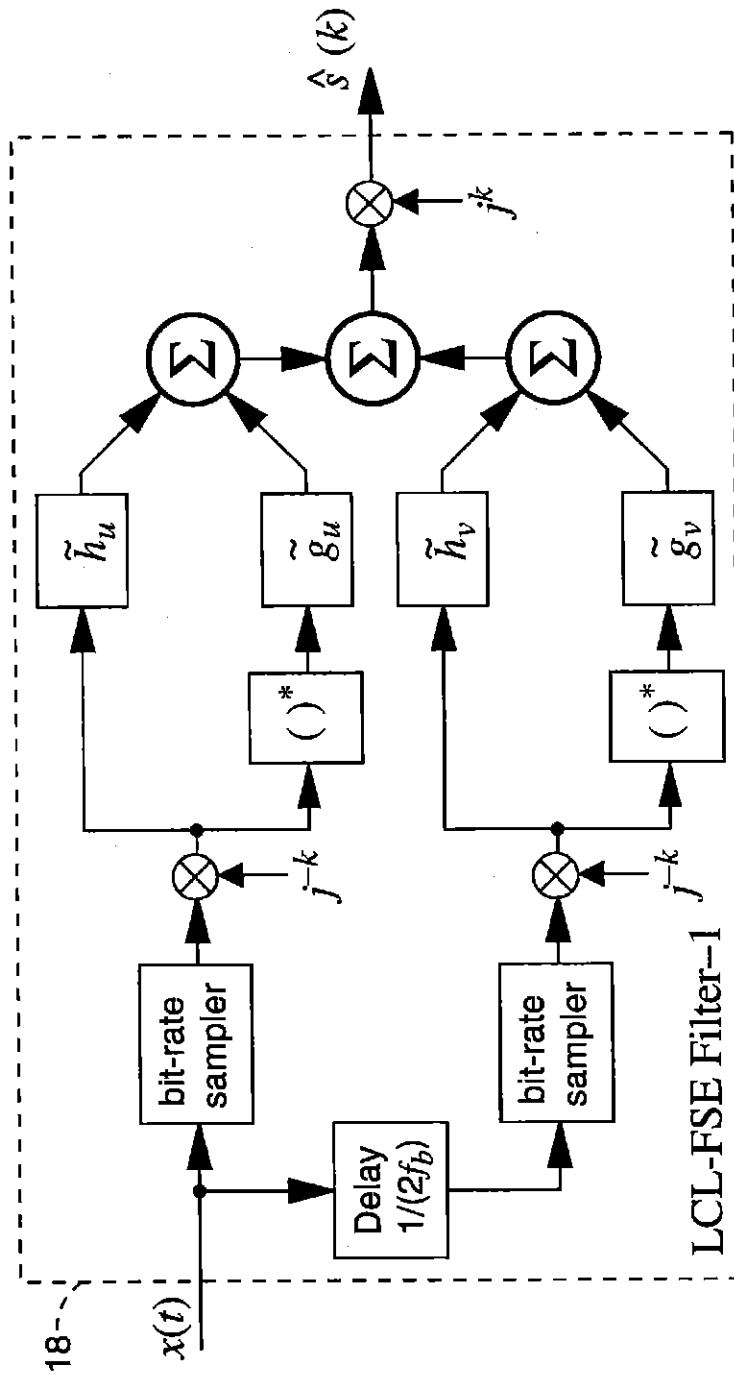


FIG. - 8

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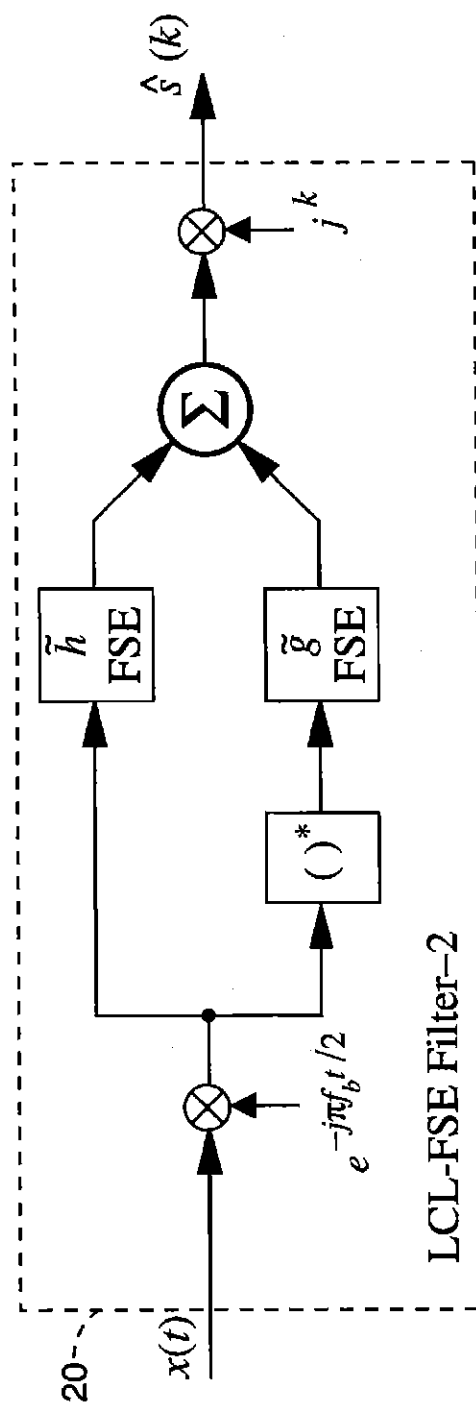


FIG. - 9

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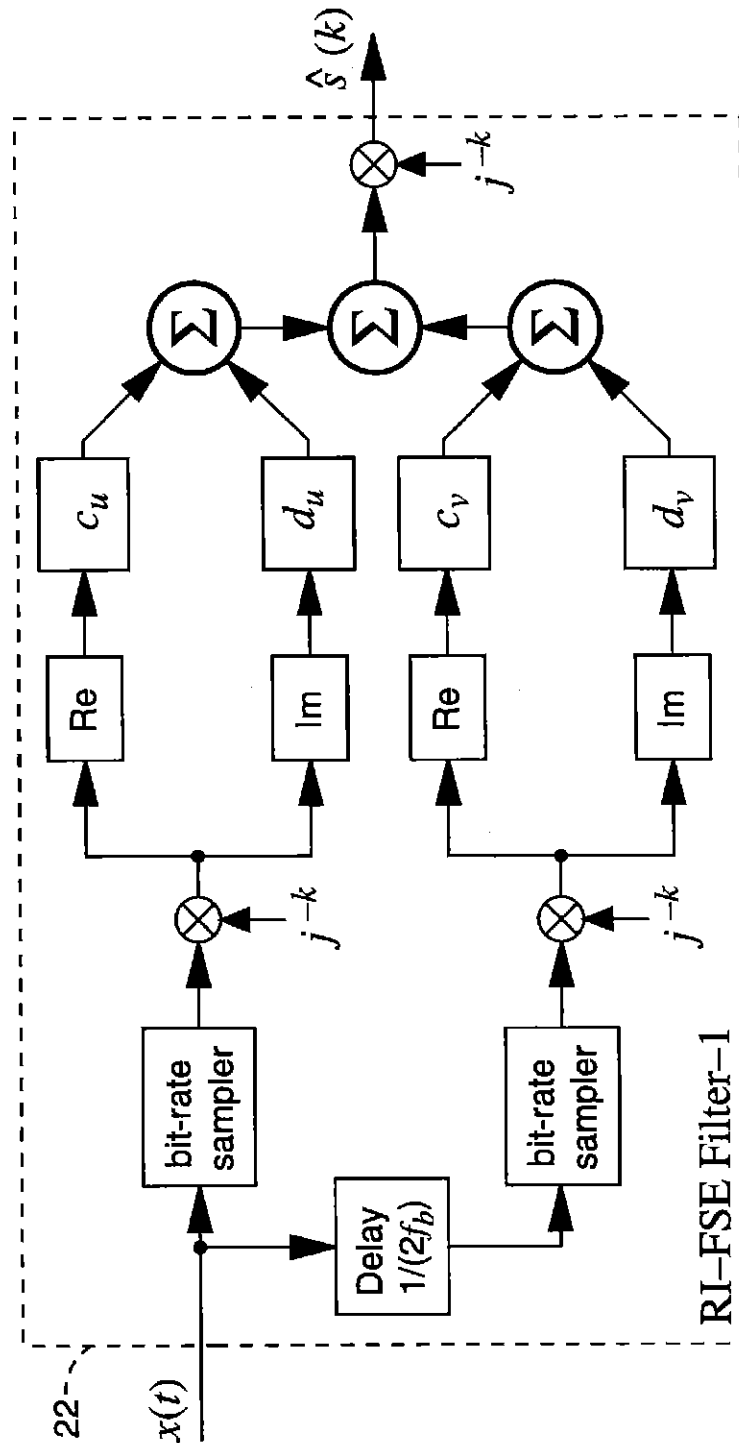


FIG. - 10

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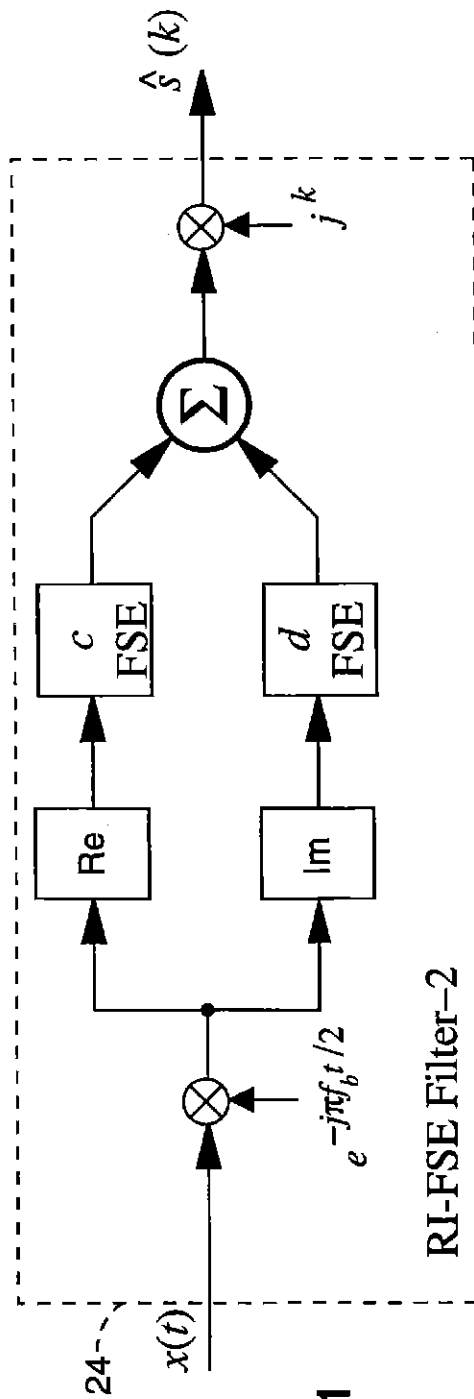


FIG. - 11

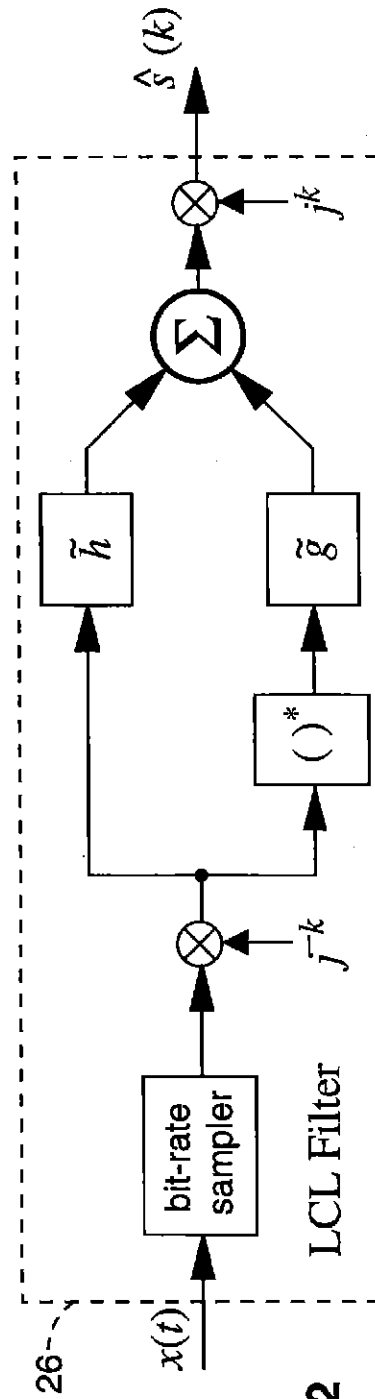


FIG. - 12

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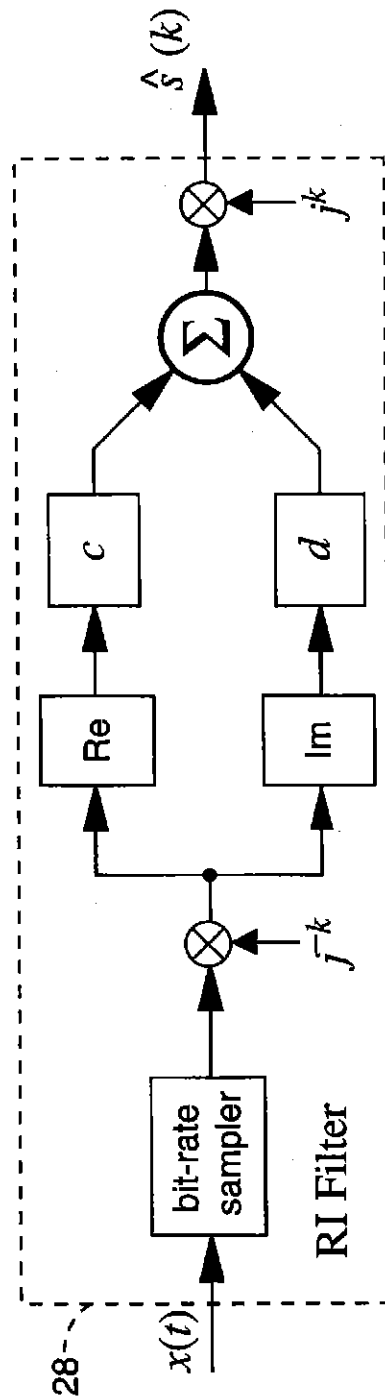


FIG. - 13

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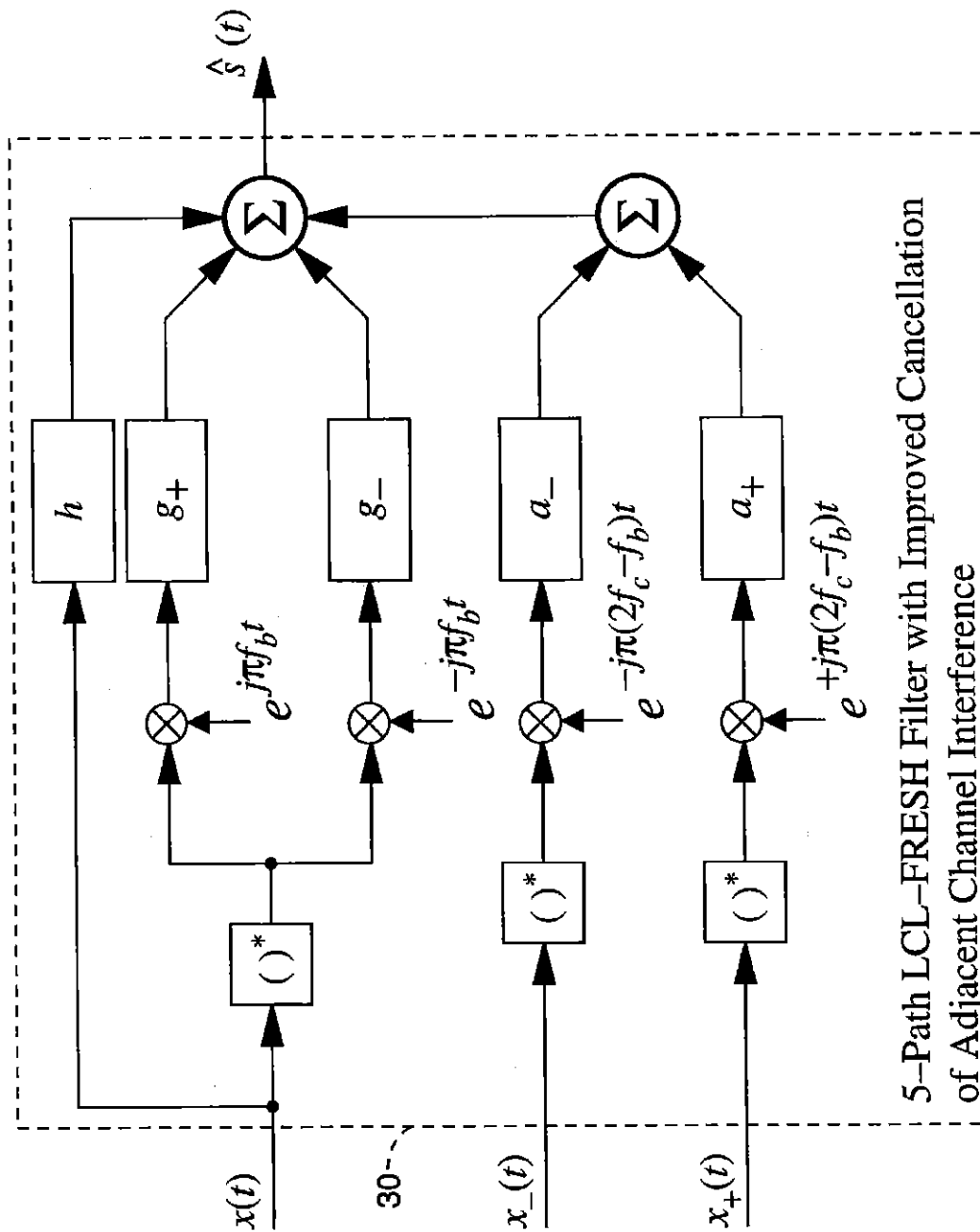


FIG. - 14

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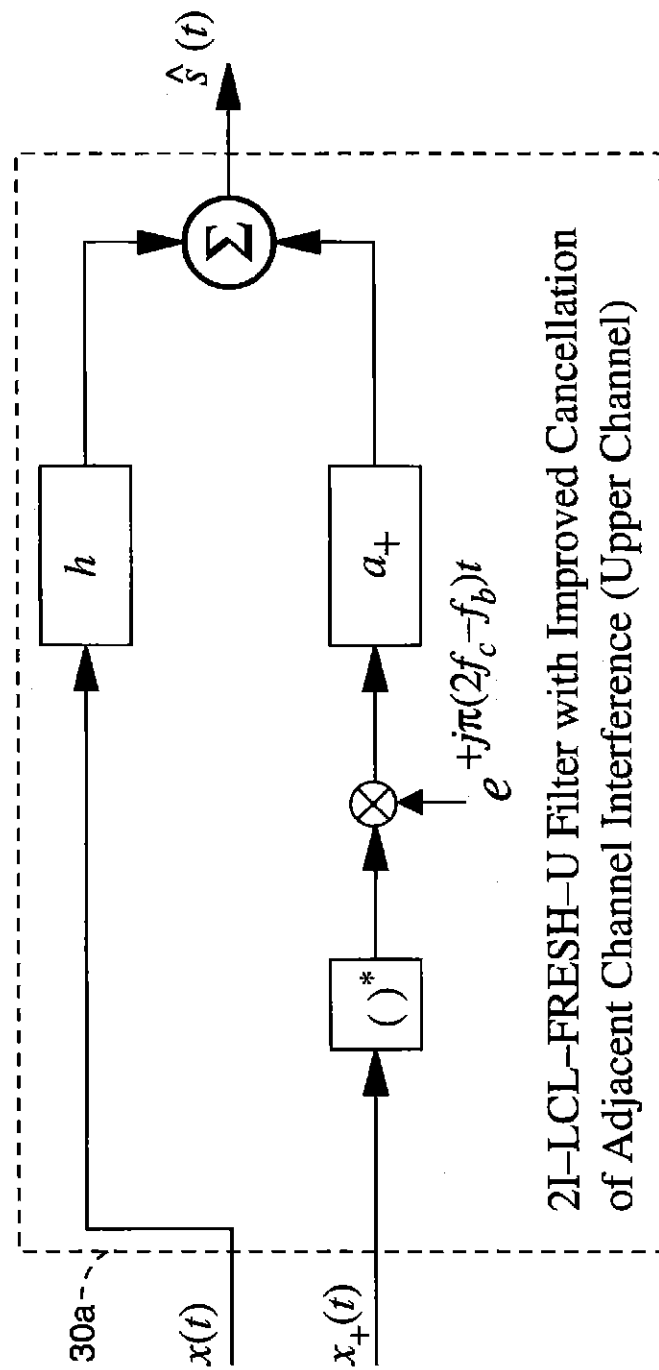


FIG. - 15

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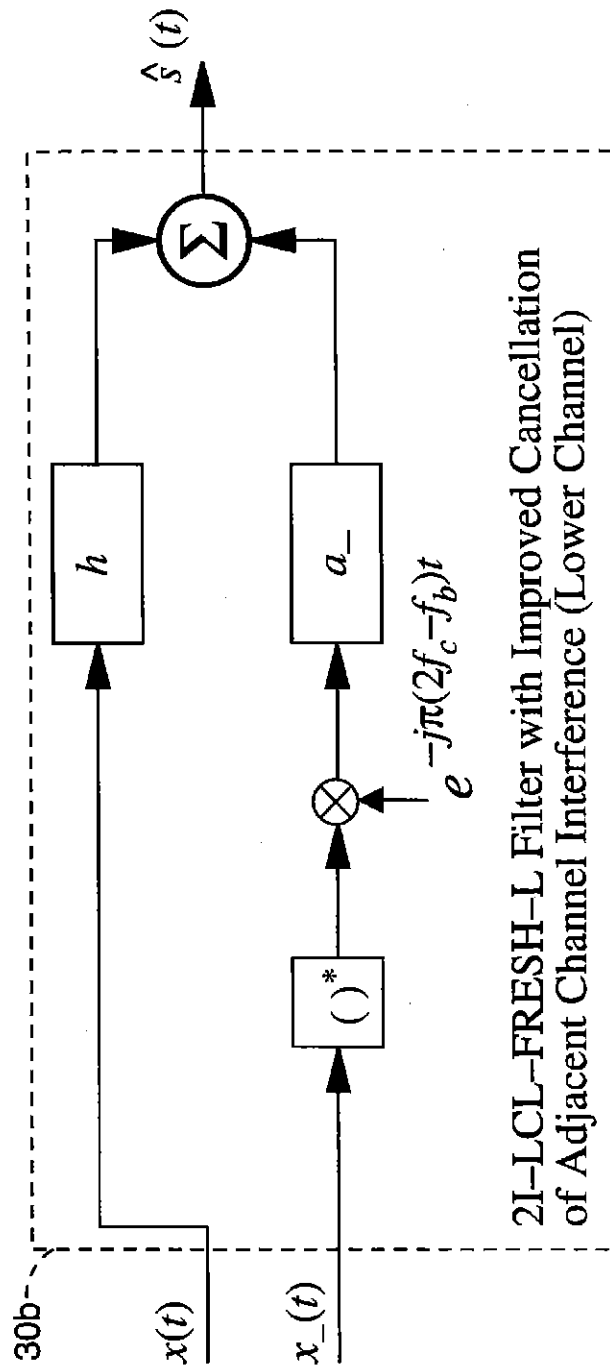


FIG. - 16

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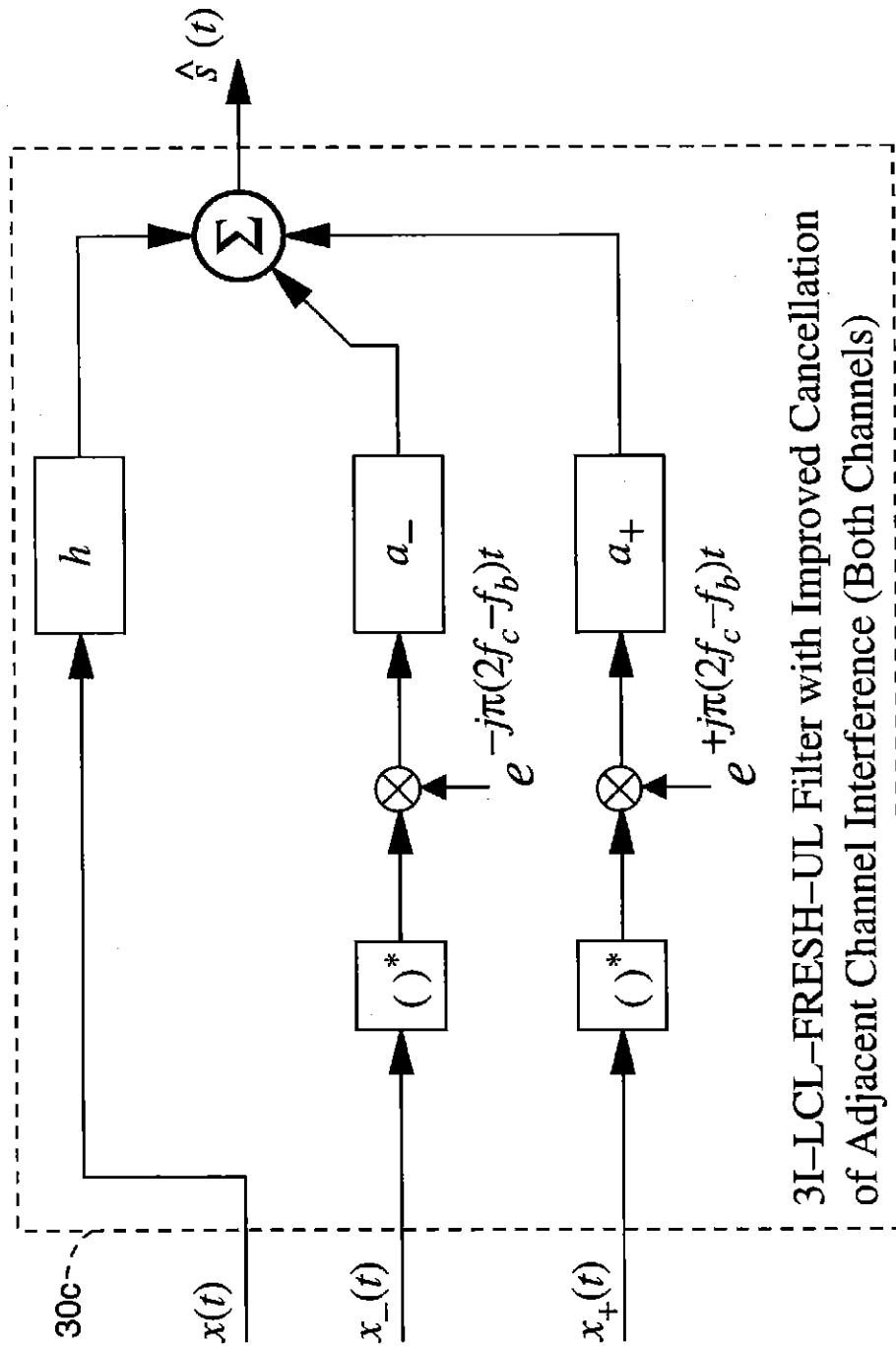


FIG. - 17

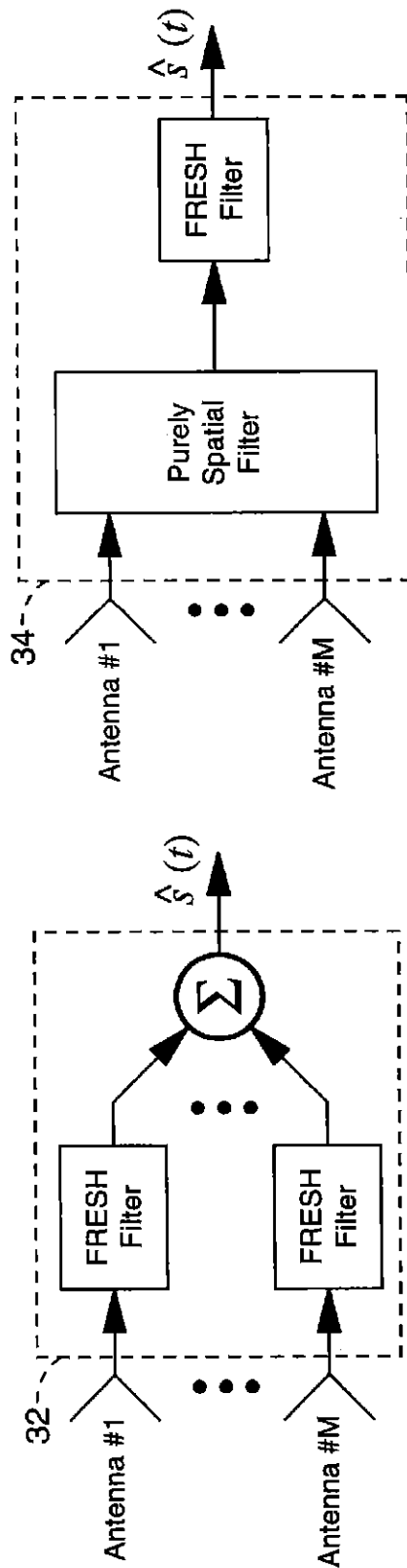


FIG. - 19

FIG. - 18

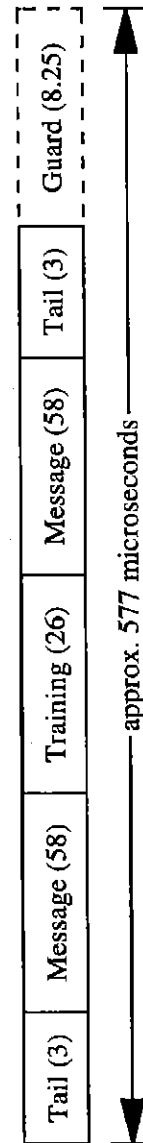


FIG. - 20

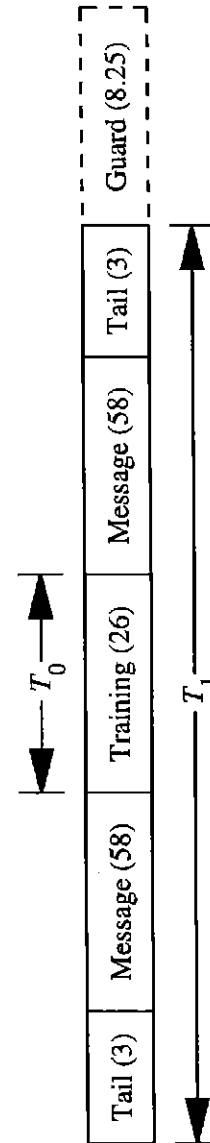


FIG. - 21

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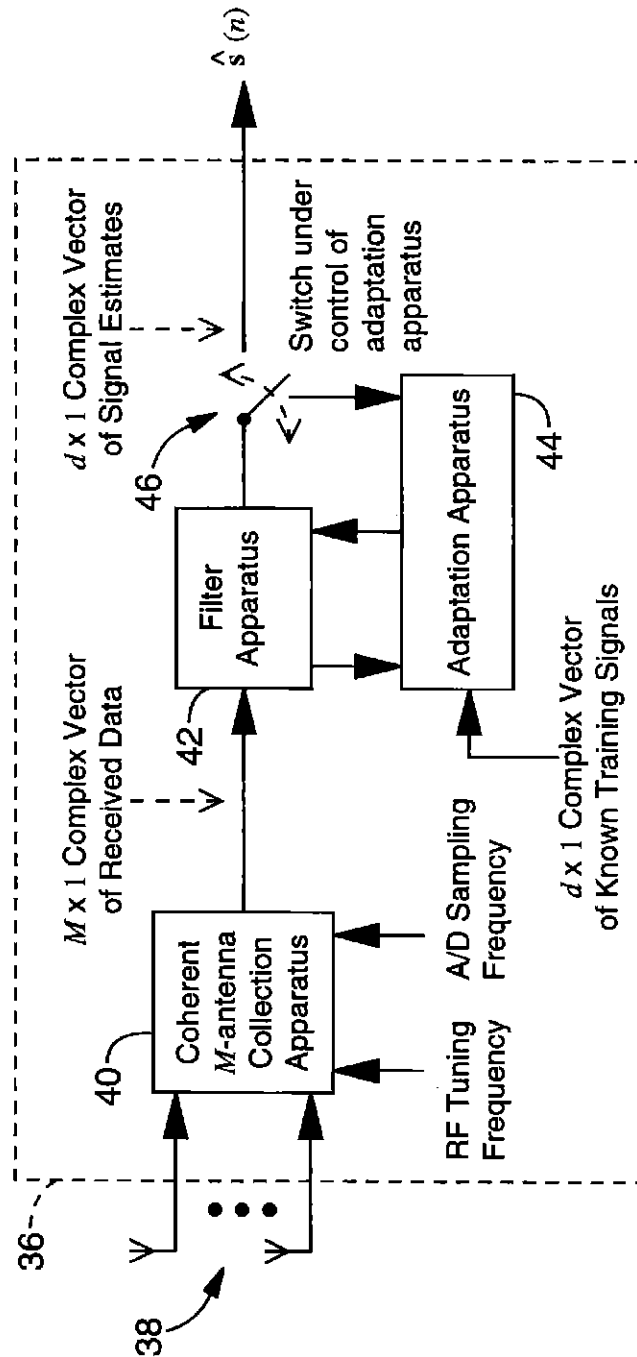


FIG. - 22

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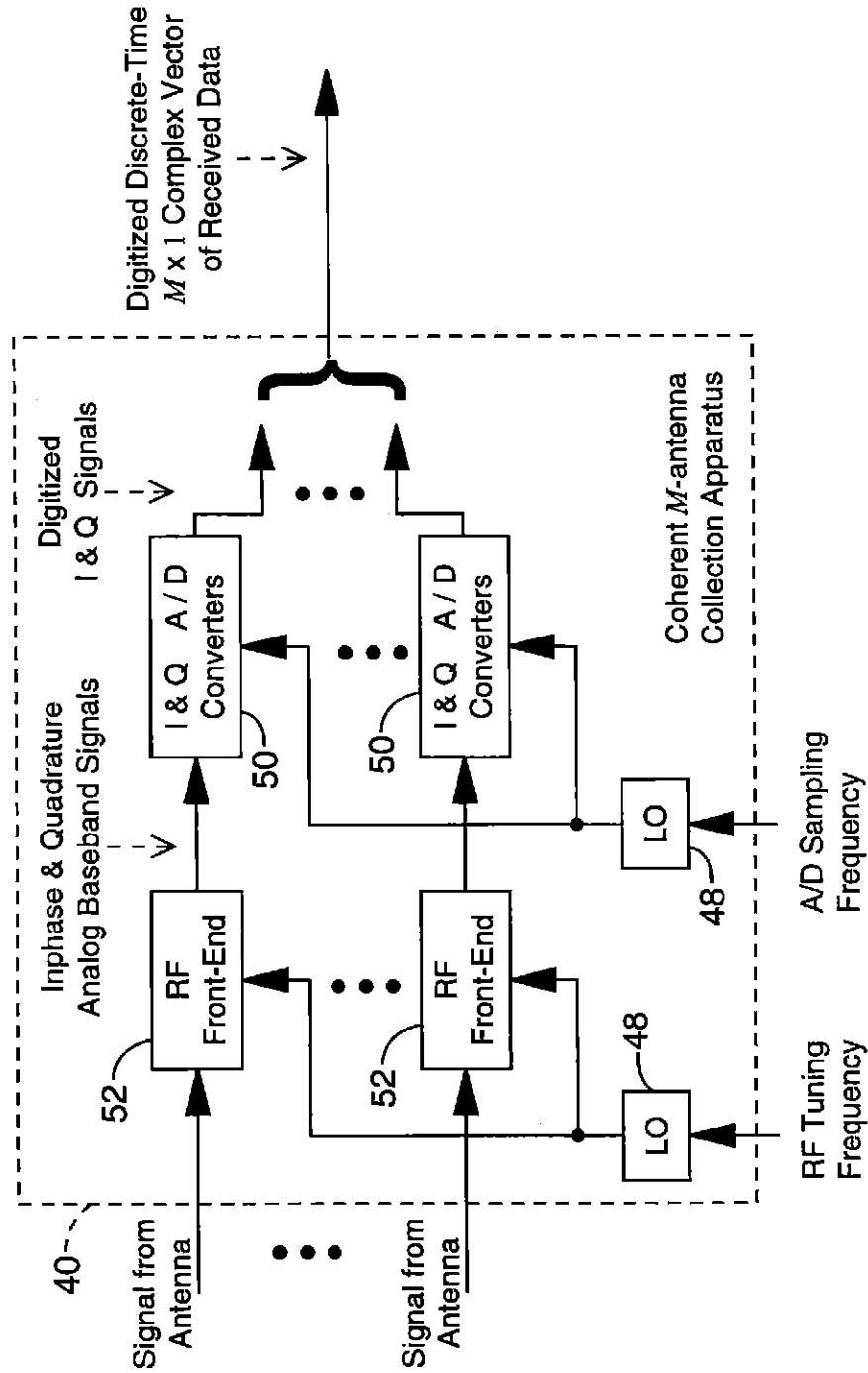


FIG. - 23

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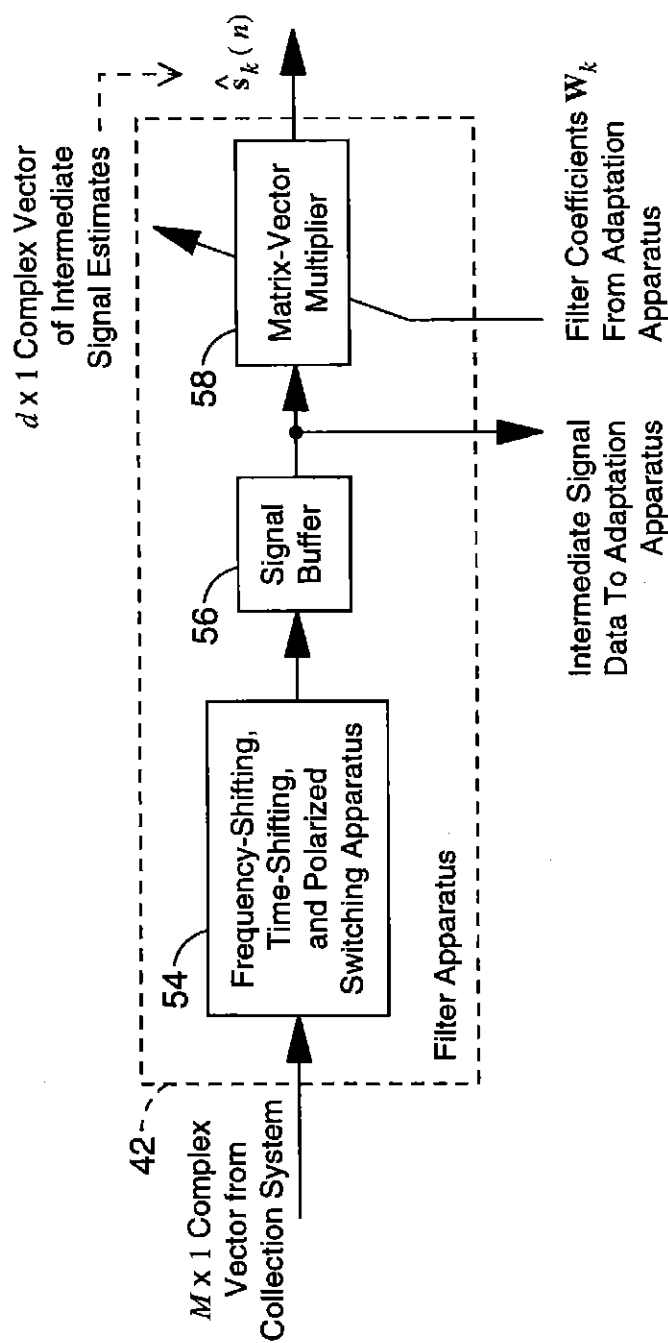


FIG. - 24

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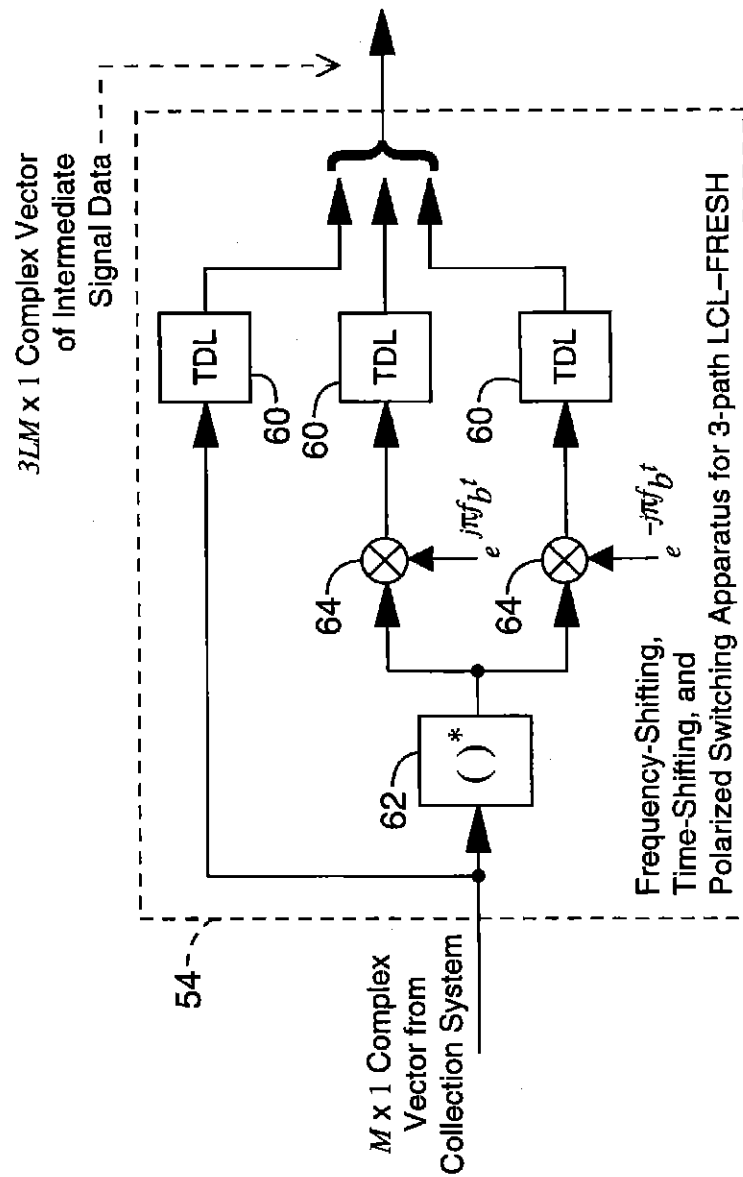


FIG. - 25

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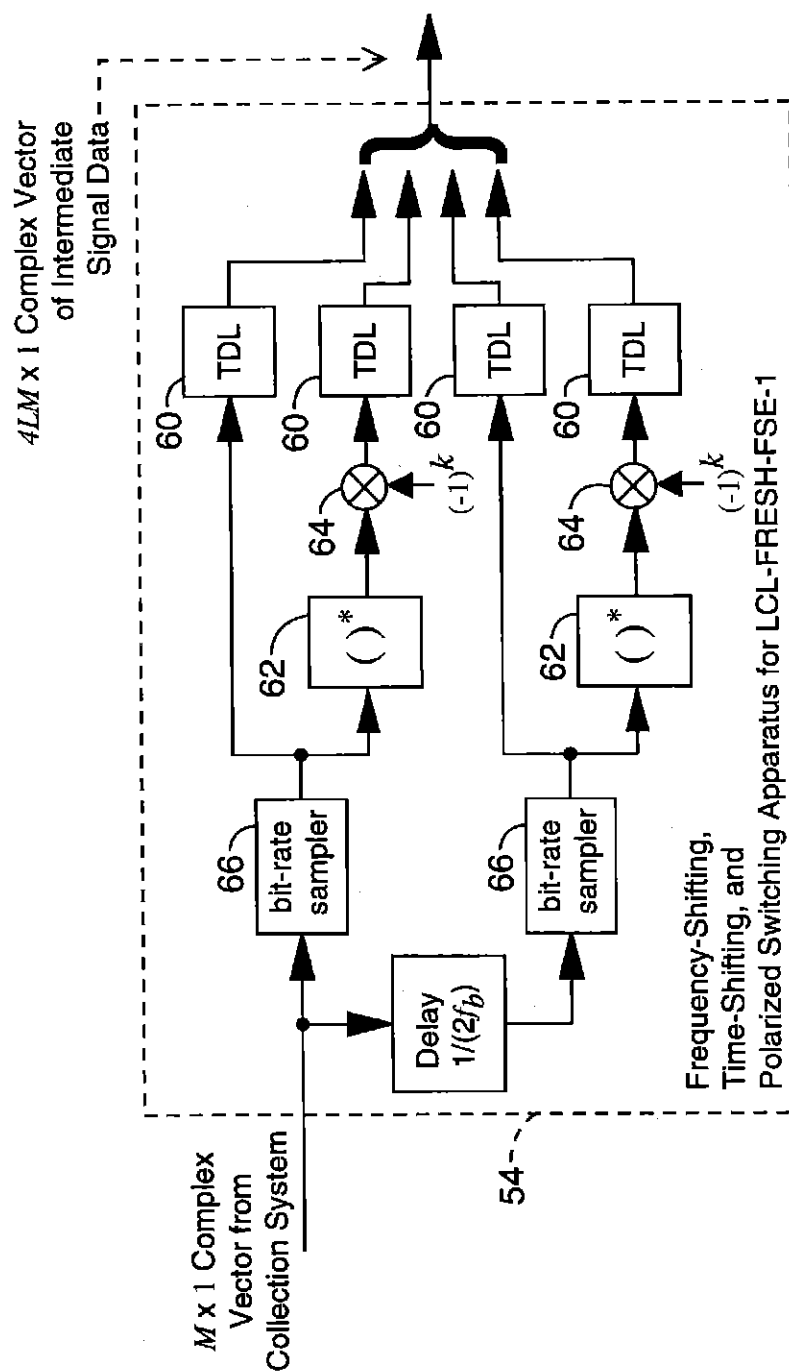


FIG. - 26

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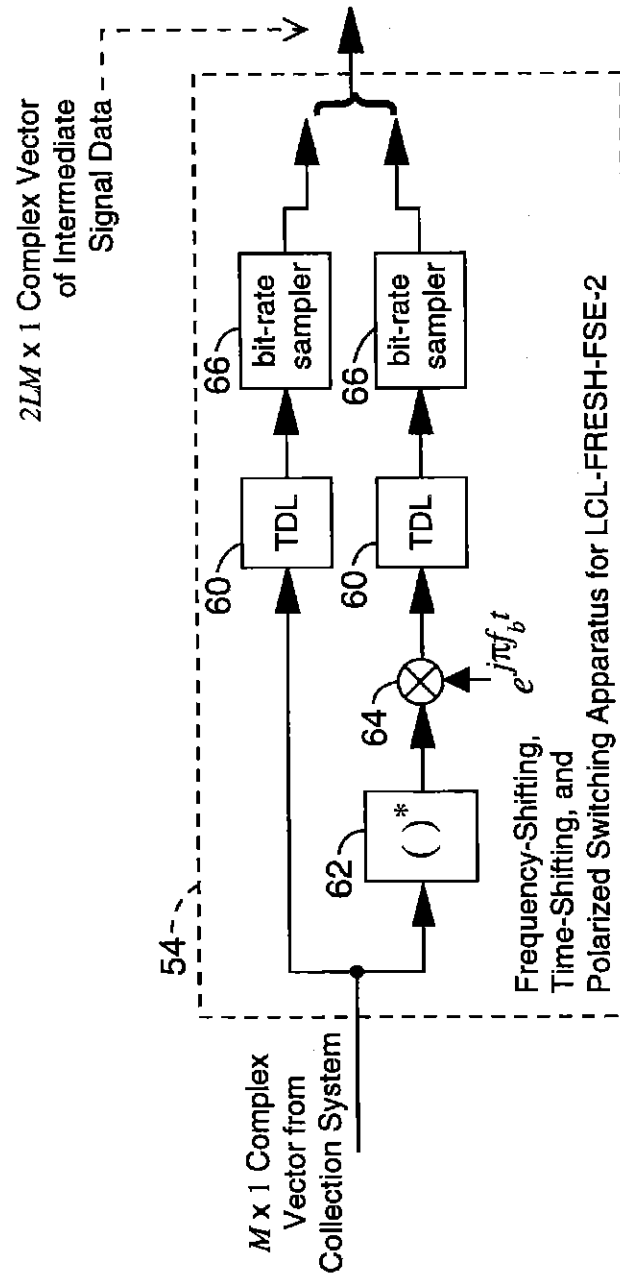


FIG. - 27

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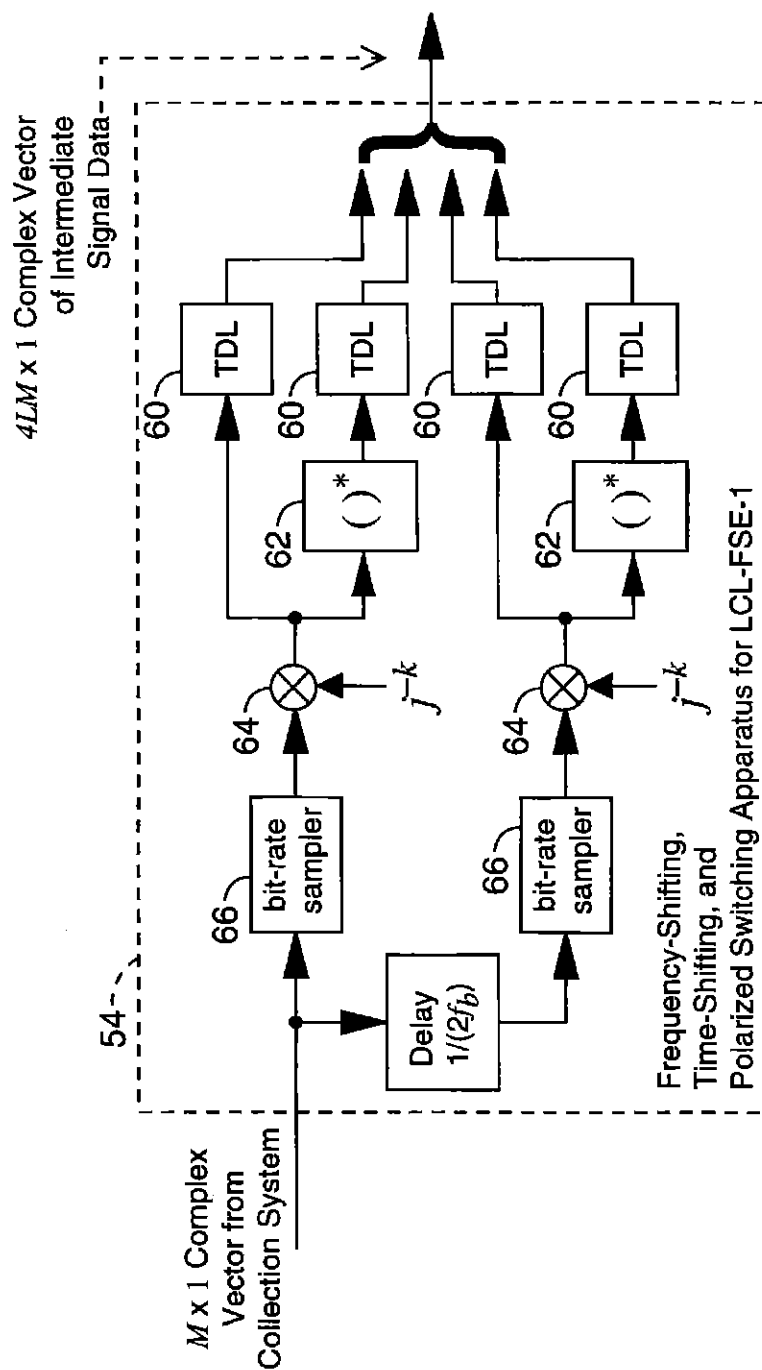


FIG. - 28

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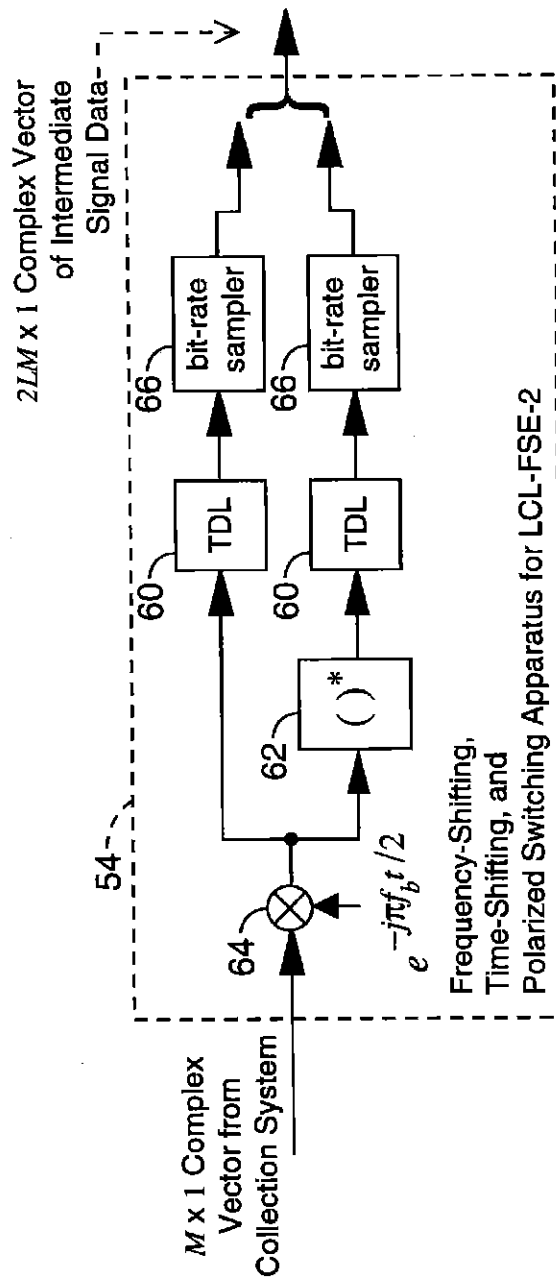


FIG. - 29

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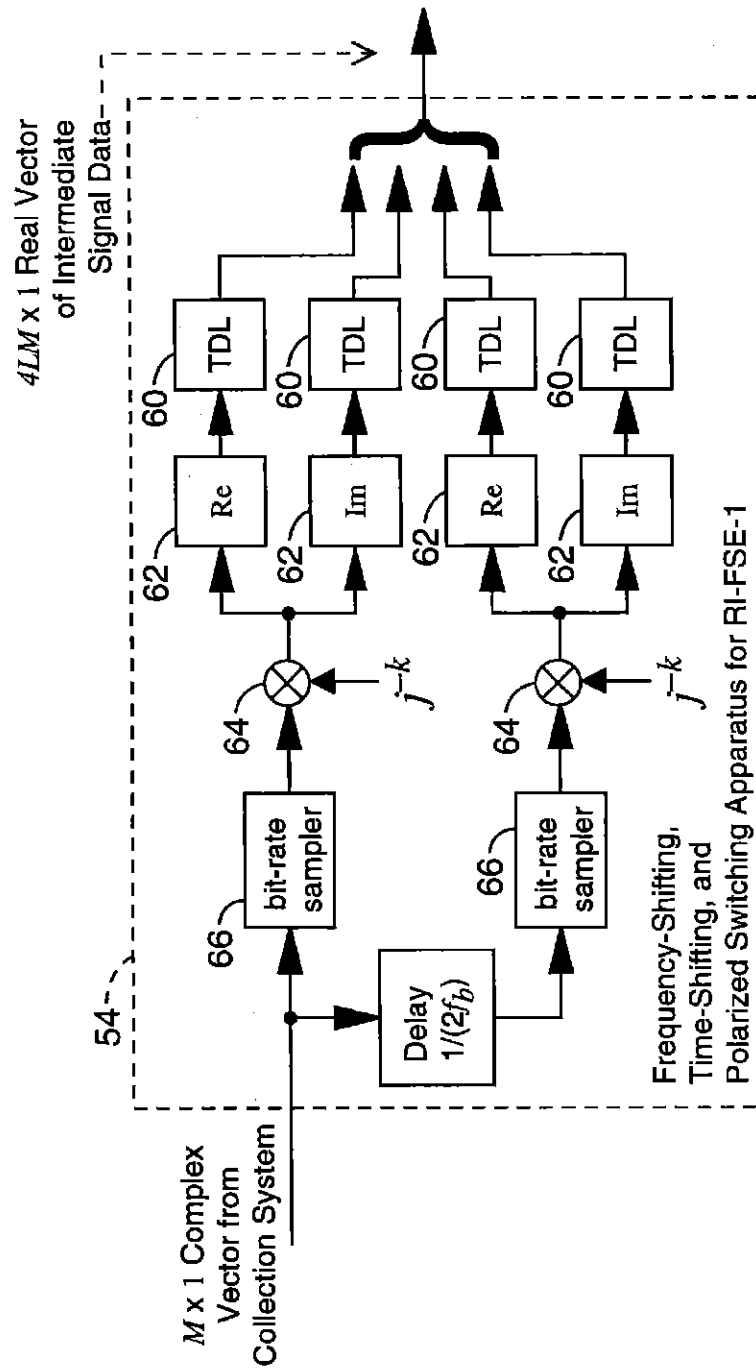


FIG. - 30

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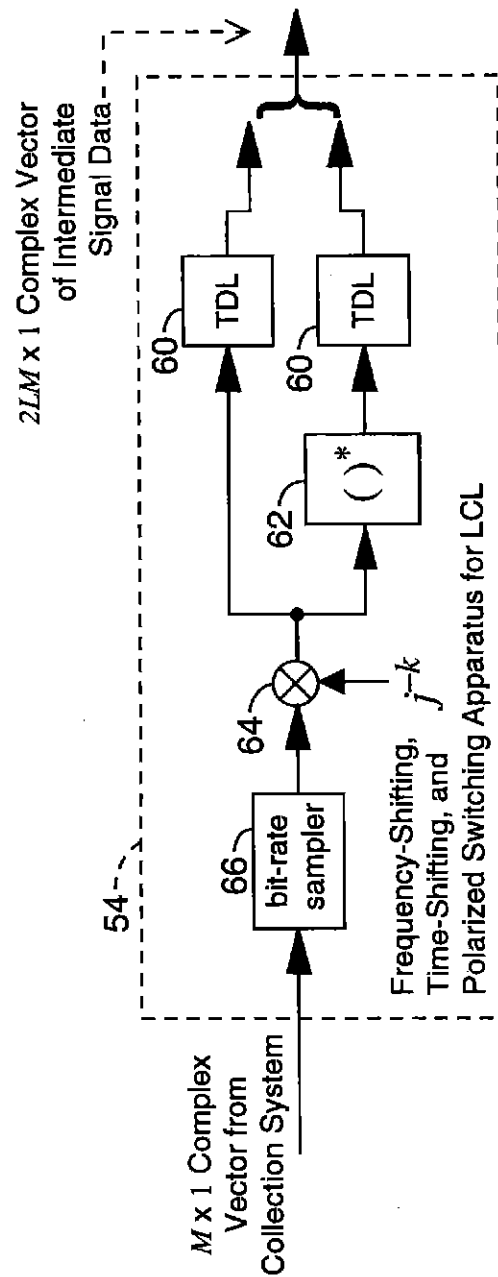


FIG. - 31

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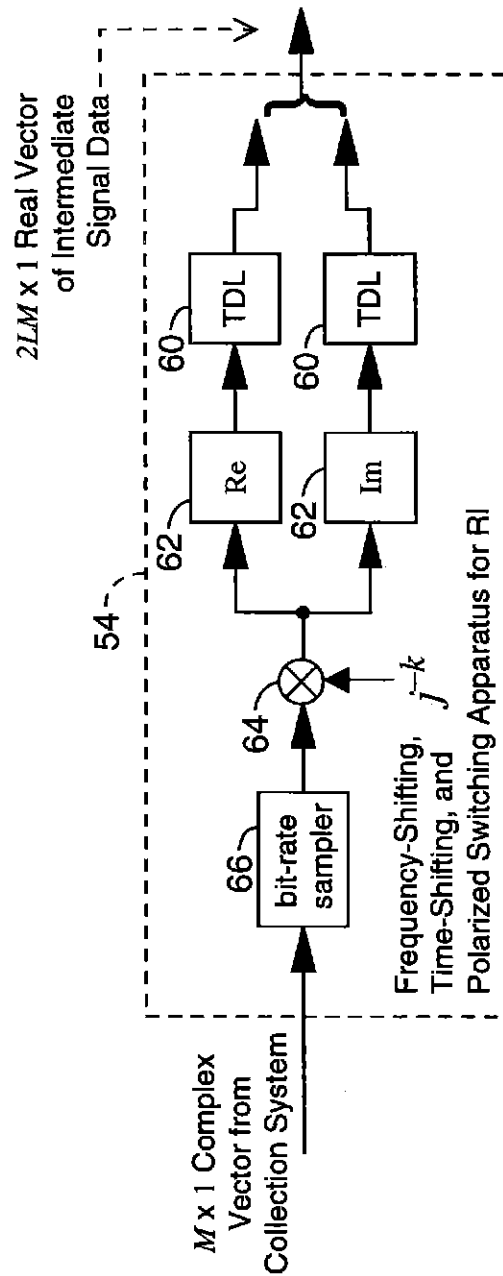


FIG. - 32

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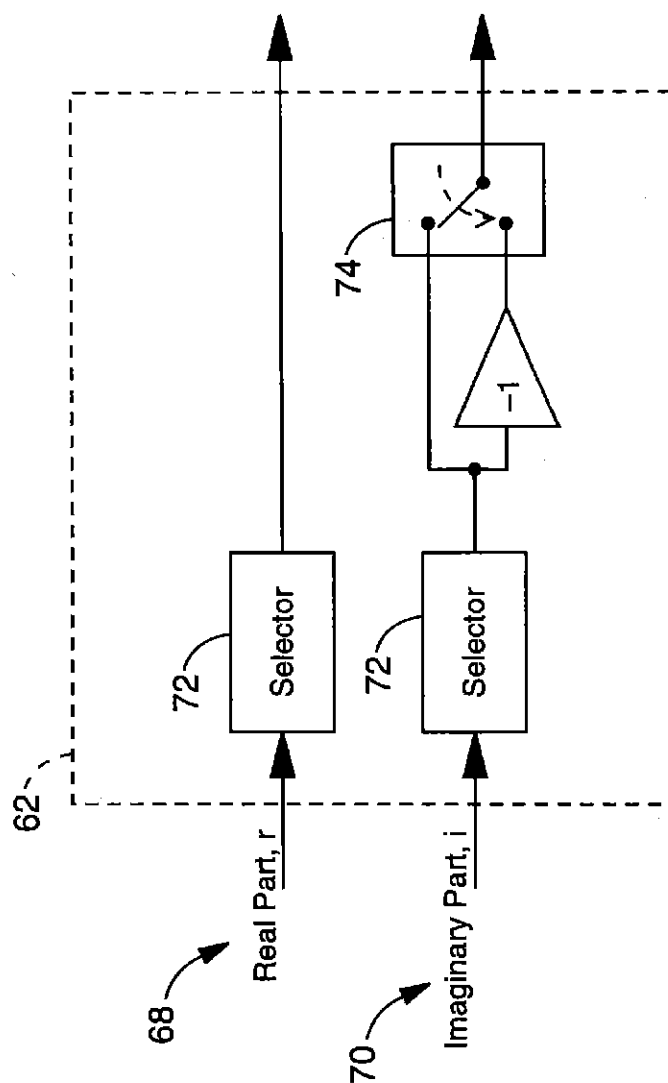


FIG. - 33

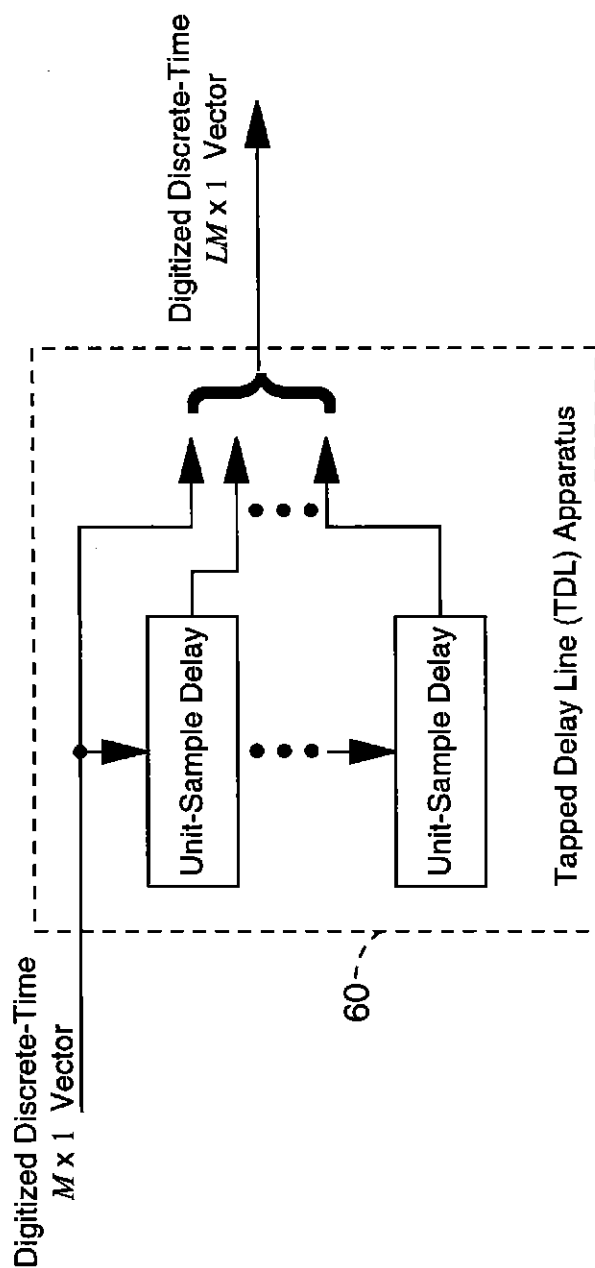


FIG. - 34

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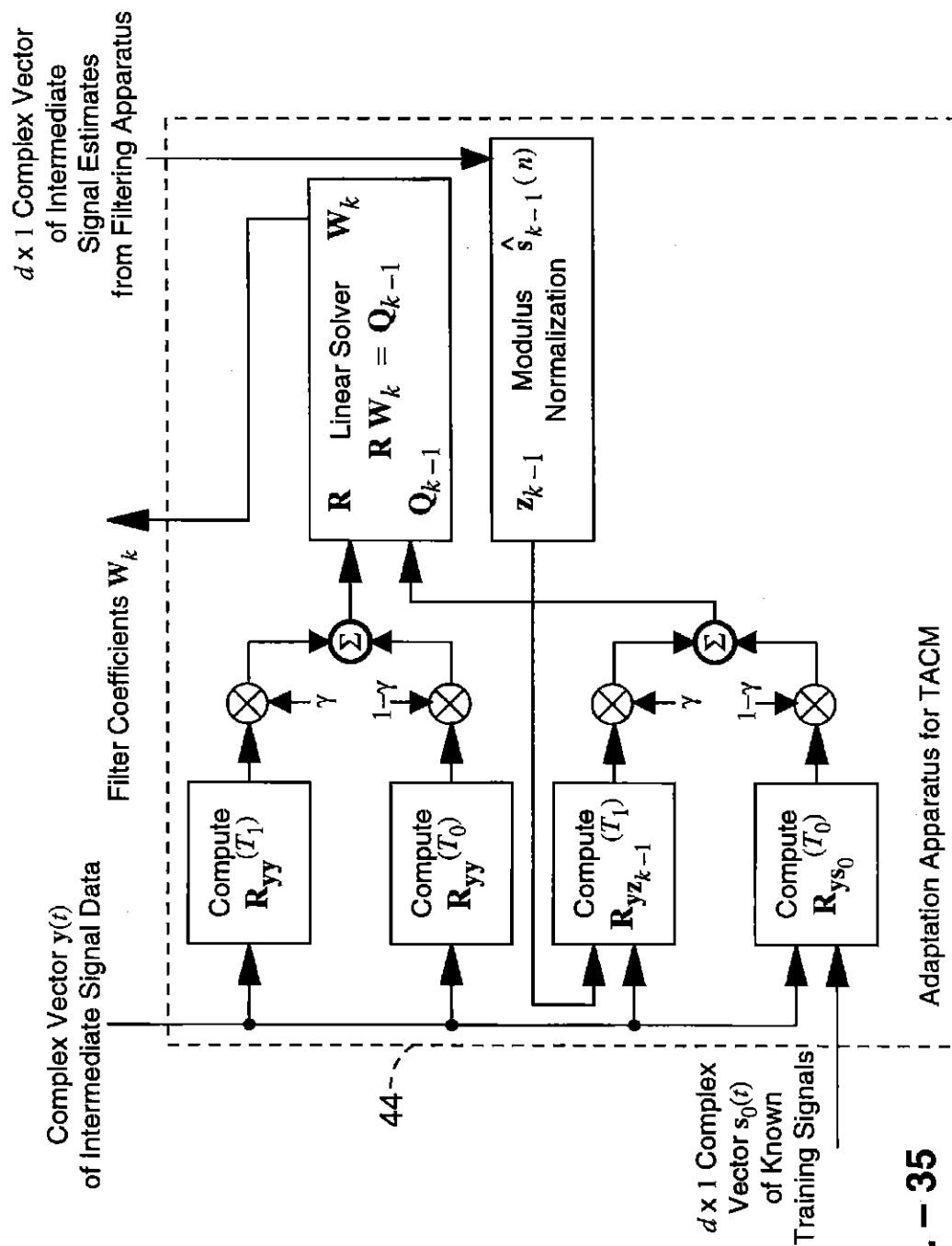


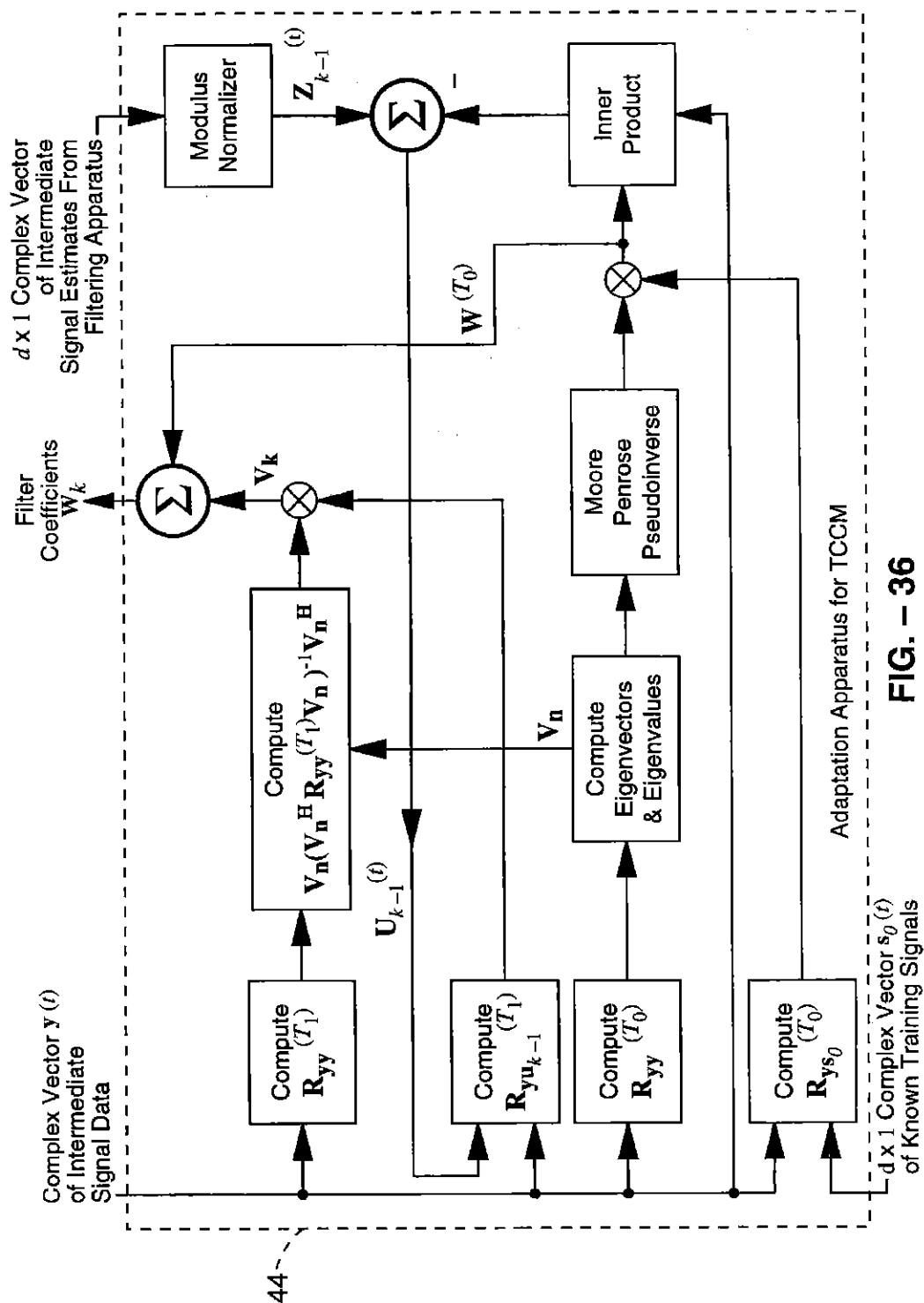
FIG. - 35

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GMSK SIGNAL PROCESSORS FOR IMPROVED COMMUNICATIONS CAPACITY AND QUALITY

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention pertains generally to reducing interference in wireless communications systems using Gaussian Minimum Shift Keyed (GMSK) or other Minimum Shift Keyed (MSK) signals, and more specifically to a method and apparatus for (i) rejecting interfering signals received at a communications receiver and occupying the same or adjacent spectral band as one or more desired signals, (ii) separating, if needed, multiple desired communication signals received at a communications receiver, and (iii) correcting distortion that affects one or more desired communication signals received at a communications receiver such as that caused by a multipath radio-frequency (RF) propagation channel.

2. Description of the Background Art

The background of the present invention falls into two general categories: (i) work pertaining to the filtering structures that perform interference rejection, signal separation, and distortion correction; and (ii) work pertaining to the adaptive algorithms that adjust the coefficients in these filtering structures to allow them to operate in signal environments that are endemic to mobile, portable, and personal wireless communication systems. This work, however, differs substantially from the present invention.

A. Filtering Structures

With the exception of the single-sensor FRESH filter and the Joint Maximum Likelihood Sequence Estimator, all prior work on interference rejection and signal separation that could be applied to a system that receives multiple GMSK or MSK signals in the same spectral band and time interval requires that multiple sensors (antennas) be used. Another requirement of this body of work is that the spatial characteristics of any two signals received by the multiple sensors must be sufficiently different; if this requirement is not met (or is poorly met) then the filtering structure will not be able to reject the interference and separate the desired signals (or will do so with poor quality). This body of work includes all filtering structures that belong to the class of linear time-invariant (LTI) spatio-temporal filters; this class of filters is defined by the fact that their outputs can be modeled mathematically as sums of amplitude-scaled, phase-shifted, and time-shifted versions of the output signals of the multiple sensors. Notably, this class of filters characterizes virtually all filtering structures that have been previously developed to address the problem (and problems similar to it) solved by the present invention.

With regard to the single-sensor FRESH filter, none of the prior works (i) discuss or describe application of FRESH filtering to GMSK or other MSK signal types, (ii) discuss or describe application to GSM or DECT standards, (iii) suggest using frequency shifts equal to plus or minus one-half the bit rate of a signal or equal to two times the carrier frequency plus or minus one-half the bit rate, (iv) show a 3-path LCL-FRESH structure relevant to multiple GMSK signals with equal carrier frequencies and bit rates which (for complex envelope with zero carrier offset) contains no unconjugated frequency-shifted paths, (v) show a 2-path, 2-input LCL-FRESH structure relevant to adjacent-channel GMSK interference, (vi) show a 3-path, 3-input LCL-FRESH structure relevant to adjacent-channel GMSK interference, (vii) show a multi-sensor 3-path LCL-FRESH

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structure comprising one each of identical versions of a 2-path or 3-path LCL-FRESH structure at the output of each sensor with all 2-path LCL-FRESH filter outputs summed, (viii) describe any partially blind methods for adaptation of any filter to separate GMSK or MSK signals, (ix) describe any training-aided constant modulus algorithm (CMA) methods for adaptation for any signal type, (x) demonstrate separability of any GMSK or MSK signals using any FRESH filtering method, (xi) describe the theoretical spectral redundancy properties of GMSK that provide for signal separability using FRESH filtering, or (xii) show the measured spectral redundancy properties of GMSK signals. Furthermore, the single-sensor FRESH filter has a severe limitation that is transcended by the present invention: it cannot accommodate multiple sensors in any way that allows it to reject and/or separate more than 2 co-channel and 2 adjacent-channel GMSK or MSK signals, in contrast to the present invention.

Although a Joint Maximum Likelihood Sequence Estimator (JMLSE) operating with a single sensor may theoretically have the ability to reject and/or separate more than 2 GMSK or MSK signals, the quality of the separated signals must degrade substantially when the number of these exceeds two. Furthermore, the computational complexity of the JMLSE is extremely high and is not practical to implement in real-time without expensive hardware that draws copious amounts of electrical energy. These drawbacks become more severe (at an exponential rate) as the number of sensors to be accommodated increases beyond one and the number of signals to be jointly separated increases.

The filtering structures of the present invention do not suffer from the severe implementational drawbacks of the JMLSE; they accommodate single or multiple sensors to great advantage; and they can reject and/or separate multiple signals even when one or more pairs of these signals cannot be rejected and/or separated by the LTI spatio-temporal filters. More specifically, the filtering structures of the present invention provide capabilities, for rejecting interfering signals and separating desired signals and correcting their distortion, that are not provided by any prior filtering structures.

B. Adaptation Methods

The filtering structures used in this invention, and in other means and apparatus for interference rejection, signal separation, and correction of distortion are controlled through the selection of values of filter parameters or coefficients. In some embodiments of these filtering structures, these coefficients can include the gains and phases used to weight various signals prior to summing. In typical wireless communication systems, the values of these coefficients must vary properly because the spatio-temporal characteristics of the received signals vary in time. That is, the values of the coefficients of these filtering structures must be chosen adaptively.

With the exception of the present invention, there are two classes of methods for adaptively choosing these coefficient values: (i) conventional methods that rely exclusively on the knowledge of training sequences embedded in the desired signals, this knowledge being used to choose an initial setting for the coefficient values, typically according to a least-squared error criterion; and (ii) blind methods that do not use embedded training sequences but instead exploit one or more properties of the interference or of the desired signals.

Conventional methods that use a known training signal are well-known to those skilled in the art of signal

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processing, and are described in the open literature pertaining to adaptive signal processing and adaptive filtering. Conventional methods are limited in their applicability, however, because the number of coefficient values that they can adapt successfully must be less (much less for good performance) than the number of known samples within the embedded training sequence. This number of known samples is fixed by the wireless communication system (e.g., for all signals in GSM that convey voice traffic, this number is the product of 26 and the number of effectively independent samples per bit (which is typically 2) used in the digitization of the continuous-time received signals, assuming a discrete-time implementation). With regard to the problem addressed by the present invention, in which the filtering structures can have, potentially, a large number of adjustable coefficients, the number of known samples within the embedded training sequence can be insufficient to provide a useful initial setting. A filtering structure initialized in this way would not provide sufficiently high quality estimates of the desired signals to be useful in the communications system.

In contrast, blind methods can make use of the entire record of the received signals over which the spatio-temporal characteristics (i.e., angles of arrival, relative delays, and relative phases of the multiple propagation paths of the multiple signals, and carrier and bit phases of the transmitted signals) are approximately constant. This circumvents the limit imposed by conventional methods on the number of filter coefficients that can be adapted. However, the various blind methods have other severe drawbacks that prevent them from operating successfully in many situations, including the one addressed by the present invention. Specifically, the class of Constant Modulus Algorithms (CMA) and related methods can require much longer data records, to converge to a high-quality solution, than are available. Furthermore, their convergence to a solution is not guaranteed, leading to a confusion between interfering signals (which should be rejected) and desired signals (which should be separated from each other and from the interference). Another class of blind methods relies on estimating the directions of arrival of the received signals; these estimates are then used in a table lookup (through the array manifold, which is a table of calibration data for the multiple-sensor receiver) to find the coefficients of an LTI spatio-temporal filter. However, these so-called direction-finding methods tend to perform poorly in the presence of severe multipath, unknown number of signals, and unknown interference and noise fields, these three impairments being endemic to the wireless communication systems of interest in the present invention.

Furthermore, the direction-finding methods do not provide the types of parameter estimates that are needed to adapt the filtering structures of the present invention.

The adaptation methods of the present invention exploit in a novel manner both the embedded training sequences and the constant modulus property of the desired signals, and in doing so circumvent the undesirable aspects of the conventional methods and of the blind methods. That is, the adaptation methods of the present invention, which choose the values of coefficients in filtering structures, such as those of the present invention, provide capabilities that are not provided by prior adaptation methods.

SUMMARY OF THE INVENTION

The present invention provides a method and apparatus for rejecting undesired interference and separating and cor-

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recting the distortion of desired interfering signals of the Gaussian Minimum Shift Keyed (GMSK) or other Minimum Shift Keyed (MSK) type, which arise for example in wireless communication systems based on either the Digital European Cordless Telecommunications (DECT) standard or the Global System for Mobile Communications (GSM) standard and, as a result, improving the quality and quantity of service offered by a wireless communications system that uses GMSK or other MSK signals.

The utility of the present invention may be appreciated, for example but not exclusively, by manufacturers of wireless communications equipment and providers of wireless communications services who wish to improve the quality of communications offered to users of their systems, and/or who wish to increase the number of users of their systems (e.g., to increase revenue in a commercially operated system).

Quantity of service improves when a base station is able to support a greater number of users for a fixed frequency allocation by, for example, being able to reduce frequency reuse distance, extend range, possibly reuse frequencies within a cell, and more frequently assign users to adjacent channels. Quality of service improves, for example, when requests made to a central coordinator (hereafter referred to as the base station) by users to initiate calls are rejected less frequently (i.e., blocking probability is reduced), when calls in progress by mobile users are not abnormally terminated (i.e., probability of dropped calls is reduced), and when the intelligibility of the voice messages in the call is better (i.e., the parties to the call hear less noise, interference, abnormal silences, and the like).

The present invention addresses these general problems by means of a method and apparatus that perform the following functions: (1) rejection of interfering signals received at a communications receiver and occupying the same (or adjacent) spectral band as one or more desired signals, (2) separation (if needed) of multiple desired communication signals received at a communications receiver, and (3) correction of distortion that affects one or more desired communication signals received at a communications receiver (e.g., distortion caused by a multipath radio-frequency (RF) propagation channel).

Accordingly, an object of the invention is to provide an array of one or more antennas whose outputs are processed by conventional RF front-ends and are then processed by a filtering structure that rejects interference, separates one or more desired signals, and corrects distortion to obtain high-quality estimates of desired signals.

Another object of the invention is to provide a class of filtering structures that can reject interfering signals, separate one or more desired signals, and correct the distortion in these desired signals.

Another object of the invention is to provide a class of filtering structures that can operate on the outputs of any number of antennas after these outputs are downconverted by conventional RF front-ends, including the special case when only one antenna is used.

Another object of the invention is to provide a linear-conjugate-linear (LCL) frequency-shift (FRESH) filter that uses the particular pattern of frequency-shifts and conjugations to exploit the cyclostationarity properties that are characteristic of GMSK and other MSK signals, thereby providing the desired interference-rejection, signal-separation, and distortion-correction capabilities.

Another aspect of the invention is to provide a linear-conjugate-linear polyperiodically time-variant (LCL-PTV)

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filter that uses the particular pattern of conjugations and time-variant filter coefficients to exploit the cyclostationarity properties that are characteristic of GMSK and other MSK signals, thereby providing the desired interference-rejection, signal-separation, and distortion-correction capabilities.

Another object of the invention is to provide an LCL-FRESH fractionally-spaced equalizer (LCL-FRESH-FSE) that operates on higher-than-bit-rate-sampled data to provide bit-rate-sampled output and uses the particular pattern of frequency-shifts and conjugations to exploit the cyclostationarity properties that are characteristic of GMSK and other MSK signals, thereby providing the desired interference-rejection, signal-separation, and distortion-correction capabilities.

Another object of the invention is to provide an LCL-FSE filter that operates on higher-than-bit-rate-sampled and frequency-shifted (by one-fourth of the bit rate) data and uses the sum of FSE processed data and conjugated data to exploit the cyclostationarity properties that are characteristic of GMSK and other MSK signals, thereby providing the desired interference-rejection, signal-separation, and distortion-correction capabilities.

Another object of the invention is to provide a real-imaginary (RI-FSE) filter that provides a computationally efficient alternate embodiment of the LCL-FSE filter.

Another object of the invention is to provide an LCL filter that operates on bit-rate-sampled and frequency-shifted (by one-fourth of the bit rate) data and uses the sum of linearly filtered data and conjugated data to exploit cyclostationarity properties that are characteristic of GMSK and other MSK signals, thereby providing the desired interference-rejection, signal-separation, and distortion-correction capabilities.

Another object of the invention is to provide a real-imaginary (RI) filter that provides a computationally efficient alternate embodiment of the LCL filter.

Another object of the invention is to provide a class of methods for adapting the adjustable coefficients in any of the filtering structures of this invention so that the filter can properly perform the functions of rejecting interference, separating one or more desired signals, and correcting distortion.

Another aspect of the invention is to provide a means of adapting the adjustable coefficients, by using both the known training sequence(s) embedded in the desired signal(s) and the knowledge that the desired signal(s) have constant modulus, thus providing training-augmented constant-modulus (TACM) adaptation.

Another object of the invention is to provide a means of adapting the adjustable coefficients, by using the known training sequence(s) embedded in the desired signal(s) to constrain adaptation that uses the knowledge that the desired signal(s) have constant modulus, thus providing training-constrained constant-modulus (TCCM) adaptation.

These general aspects and their details give this invention significant capabilities of great utility in wireless communication systems. The capability provided by this invention can be used to improve the quality and quantity of communication services provided by such systems. The quality of service can be improved by using the invention to suppress interference in a particular frequency band and time slot jointly referred to as a channel) and spatial cell caused by other users in the same or adjacent channels and in the same or adjacent spatial cells and to correct signal distortion; and the quantity of service can be increased by reallocating channels in a manner that intentionally results in more interfering, but separable by this invention, signals per channel and cell, in order to accommodate more users.

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These improvements are made possible by this invention because this invention, using only one sensor, can separate multiple spectrally overlapping signals of certain types and can correct even severe frequency-selective fading and other distortion and, because the number of such signals that can be separated and corrected by this invention, using multiple sensors increases proportionately with the number of sensors. In cellular communication systems that use base stations, the allocation of channels can (but need not) be made so that the introduced interference occurs only in up-link channels (from mobile units to base stations) so that the invention need only be implemented in the base station receivers and not in the mobile receivers. The invention can also be used at base stations (or at mobile units, or both) using only a single receiving antenna or multiple antennas.

With reference to the specific example of GMSK and other MSK signals, between 2 and 4 GMSK and other MSK signals in a single channel can be accommodated with one antenna, depending on how many signals arise from users in the same channel and how many arise from adjacent channels. More generally, with a number M of antennas, between 2M and 4M signals can be accommodated.

Further objects and advantages of the invention will be brought out in the following portions of the specification, wherein the detailed description is for the purpose of fully disclosing preferred embodiments of the invention without placing limitations thereon.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention will be more fully understood by reference to the following drawings which are for illustrative purposes only:

FIG. 1 is a graph showing the cross-coherence functions of a sample GMSK signal and its conjugated and frequency-shifted versions.

FIG. 2A is a graph showing the spectral content of a GMSK (0.3) signal, $x(t)$.

FIG. 2B is a graph showing the spectral content of a conjugated and frequency shifted version of the signal shown in FIG. 2A, $y_1(t)=x^*(t)e^{-j\pi f_c t}$.

FIG. 2C is a graph showing the spectral density of the degree to which x shown in FIG. 2A and y , shown in FIG. 2B are correlated.

FIG. 3A is a graph showing the spectral content of a GMSK (0.3) signal, $x(t)$.

FIG. 3B is a graph showing the spectral content of a conjugated and frequency shifted version of the signal shown in FIG. 3A, $Y_2(t)=x^*(t)e^{j\pi f_c t}$.

FIG. 3C is a graph showing the spectral density of the degree to which x shown in FIG. 3A and y_2 shown in FIG. 3B are correlated.

FIG. 4 is a functional block diagram of a 3-path LCL-FRESH filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

FIG. 5 is a functional block diagram of an LCL-PTV filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

FIG. 6 is a functional block diagram of a "type-1" LCL-FRESH-FSE filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

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FIG. 7 is a functional block diagram of a "type-2" LCL-FRESH-FSE filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

FIG. 8 is a functional block diagram of a "type-1" LCL-FSE filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

FIG. 9 is a functional block diagram of a "type-2" LCL-FSE filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

FIG. 10 is a functional block diagram of a "type-1" RI-FSE filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

FIG. 11 is a functional block diagram of a "type2" RI-FSE filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

FIG. 12 is a functional block diagram of an LCL filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion for GMSK or other MSK signals.

FIG. 13 is a functional block diagram of an RI filter in accordance with the present invention for performing interference rejection and/or co-channel separation and/or distortion correction for GMSK or other MSK signals.

FIG. 14 is a functional block diagram of a 5-path LCL-FRESH filter in accordance with the present invention for performing interference rejection and/or co-channel signal separation and/or distortion correction, with improved cancellation of adjacent channel interference, for GMSK or other MSK signals.

FIG. 15 is a functional block diagram of a 2-input, 2-path LCL-FRESH filter in accordance with the present invention for canceling adjacent upper channel interference.

FIG. 16 is a functional block diagram of a 2-input, 2-path LCL-FRESH filter in accordance with the present invention for canceling adjacent lower channel interference.

FIG. 17 is a functional block diagram of a 3-input, 3-path LCL-FRESH filter in accordance with the present invention for canceling both adjacent upper and adjacent lower channel interference.

FIG. 18 is a functional block diagram of a joint space-time multi-sensor FRESH filter in accordance with the present invention.

FIG. 19 is a functional block diagram of space-time factorized processor in accordance with the present invention.

FIG. 20 is a data structure timing diagram for a time slot in a GSM system, showing the number and location of each type of bit.

FIG. 21 is a data structure timing diagram showing the time periods T_0 and T_1 for the time slot shown in FIG. 20.

FIG. 22 is a functional block diagram of a receiver apparatus in accordance with the present invention.

FIG. 23 is a functional block diagram of an embodiment of the coherent M-antenna collection apparatus block shown in FIG. 22.

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FIG. 24 is a functional block diagram of an embodiment of the filter apparatus block shown in FIG. 22.

FIG. 25 is a functional block diagram of an embodiment of the frequency-shifting, time-shifting, and polarized switching apparatus block of the filter shown in FIG. 24 where the filter is configured as a 3-path LCL-FRESH filter.

FIG. 26 is a functional block diagram of an embodiment of the frequency-shifting, time-shifting, and polarized switching apparatus block of the filter shown in FIG. 24 where the filter is configured as a type-1 LCL-FRESH-FSE filter.

FIG. 27 is an alternative embodiment of the frequency-shifting, time-shifting, and polarized switching apparatus shown in FIG. 26 where the filter is configured as a type2 LCL-FRESH-FSE filter.

FIG. 28 is a functional block diagram of an embodiment of the frequency-shifting, time-shifting, and polarized switching apparatus block of the filter shown in FIG. 24 where the filter is configured as a type1 LCL-FSE filter.

FIG. 29 is an alternative embodiment of the frequency-shifting, time-shifting, and polarized switching apparatus shown in FIG. 28 where the filter is configured as a type2 LCL-FSE filter.

FIG. 30 is a functional block diagram of an embodiment of the frequency-shifting, time-shifting, and polarized switching apparatus block of the filter shown in FIG. 24 where the filter is configured as a type1 RI-FSE filter.

FIG. 31 is a functional block diagram of an embodiment of the frequency-shifting, time-shifting, and polarized switching apparatus block of the filter shown in FIG. 24 where the filter is configured as an LCL filter.

FIG. 32 is a functional block diagram of an embodiment of the frequency-shifting, time-shifting, and polarized switching apparatus block of the filter shown in FIG. 24 where the filter is configured as an RI filter.

FIG. 33 is a functional block diagram depicting the polarized switch portion of the frequency-shifting, time-shifting, and polarized switching apparatus shown in FIG. 24 through FIG. 32.

FIG. 34 is a functional block diagram of an embodiment of the tapped time delay (TDL) apparatus block of the frequency-shifting, time-shifting, and polarized switching apparatus shown in FIG. 25 through FIG. 32, where the input and output vectors can be real or complex.

FIG. 35 is a functional block diagram of a TACM adaptation apparatus in accordance with the present invention.

FIG. 36 is a functional block diagram of a TCCM adaptation apparatus in accordance with the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring more specifically to the drawings, for illustrative purposes the present invention is generally shown with reference to FIG. 1 through FIG. 36 where like reference numerals denote like parts. It will be appreciated that the apparatus may vary as to configuration and as to details of the parts and that the method may vary as to the steps and their sequence without departing from the basic concepts as disclosed herein.

A. Filtering Structures and Their Theoretical Foundations

The filtering structures of the present invention exploit the 100% spectral redundancy exhibited by GMSK and other MSK signals. That is, these filtering structures exploit the fact that information conveyed in one subband of the GMSK

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or other MSK signal is also conveyed in another subband. In fact, for any GMSK or other MSK signal, the waveforms in any two such related subbands are nearly perfectly correlated. Thus, this spectral redundancy augments the more widely known and exploited spatial redundancy in which the waveforms due to a given source and received by two different antennas in an array are perfectly correlated.

Just as a receiver without multiple antennas and the spatial filtering structure needed to properly combine their outputs loses out on the well-known performance gains due to the exploitation of spatial redundancy, a receiver without the filter structures of the present invention loses out on the significant performance gains due to the exploitation of spectral redundancy.

By exploiting both spatial redundancy and spectral redundancy (also referred to as spectral correlation), the filtering structures of the present invention provide at least twice the level of processing gain of an LTI spatio-temporal filtering structure. For example, an M-antenna array can separate up to 2M to 4M GMSK or other MSK signals using these filtering structures, or can separate up to the more conventionally attainable number M of GMSK or other MSK signals but can do so with significantly higher reliability and quality than a conventional spatial filter. These capabilities can be explained by reference to the spectral correlation of the GMSK or other MSK signals, and by reference to the relationship between correlation (regardless of its origin or type) and linear filtering

B. Relationship Between Correlation and Linear Filtering

Although the relationship between correlation and linear filtering is fundamental, it is not widely understood even by those skilled in the art of signal processing. Therefore, for purposes of describing the present invention we will explain this relationship by a simple, although abstract, analogy: if $x(t) = a(t) + b(t)$, and $y(t) = c(t) + d(t)$, where $a(t)$ and $c(t)$ are highly correlated, and where $b(t)$ and $d(t)$ are highly correlated, then an appropriate linear combination of $x(t)$ and $y(t)$ (i.e., the output of an appropriately chosen linear filtering structure applied jointly to $x(t)$ and $y(t)$) can provide high-quality estimates of one or more of the signals $a(t)$, $b(t)$, $c(t)$, and $d(t)$.

C. Spectral Correlation of GMSK and Other MSK Signals

To understand how such performance can be possible, and to understand what filtering structures are applicable to GMSK and other MSK signals, it is necessary to understand the spectral correlation properties of GMSK and other MSK signals. For this purpose, these properties are best described by the cross-coherence function, which is a frequency-domain cross-correlation coefficient. Specifically, let $x(t)$ denote the complex envelope of a GMSK signal having zero carrier-frequency offset and bit rate f_b , and let $y(t)$ be a conjugated and frequency-shifted version of $x(t)$, where the frequency-shift is either $f_b/2$ or $-f_b/2$:

$$y_1(t) = x^*(t) e^{-j2\pi f_b t/2} \quad (1)$$

$$y_2(t) = x^*(t) e^{+j2\pi f_b t/2} \quad (2)$$

with $y(t) = y_1(t)$ or $y(t) = y_2(t)$. We consider the cross-coherence function $C_{xy}(f)$ defined by

$$C_{xy}(f) = \frac{S_{xy}(f)}{\sqrt{S_{xx}(f) S_{yy}(f)}} \quad (3)$$

where $S_{xy}(f)$ denotes the cross spectral density between $x(t)$ and $y(t)$, and $S_{xx}(f)$ and $S_{yy}(f)$ denote the power spectral densities of $x(t)$ and $y(t)$, respectively. The magnitude $|C_{xy}(f)|$

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versus frequency f for a GMSK signal is shown in FIG. 1, where the solid line represents $|C_{xy_1}(f)|$ and the dashed line represents $|C_{xy_2}(f)|$, and is clearly very nearly equal to one over the entire frequency channel (-100 kHz to $+100$ kHz) allocated to the signal.

For any f such that $|C_{xy}(f)|$ is nearly equal to one, the narrowband components of $x(t)$ and $y(t)$ at frequency f are nearly perfectly correlated. For our definitions of $x(t)$ and $y(t)$, this is equivalent to stating that the narrowband component of $x(t)$ at f and the narrowband component of $x^*(t)$ at either $f + f_b/2$ or $f - f_b/2$ are nearly perfectly correlated. Referring to FIG. 2A through FIG. 2C, the fact that $|C_{xy}(f)|$ in FIG. 2C is nearly equal to unity for narrowband components numbered $-6, -5, \dots, -1, 0$ implies that each pair of like-numbered narrowband components taken from $x(t)$ in FIG. 2A and $y_1(t) = x^*(t) e^{-j\pi f_b t}$ in FIG. 2B are nearly perfectly correlated. The same is true for the continuum of pairs of narrowband components in $x(t)$ and $y_1(t)$ at identical values off. Accordingly, an appropriate linear combination of one of these components from $x(t)$ and the corresponding one from $y_1(t)$, can provide the same level of processing gain against other signals, interference, and noise as an appropriate linear combination of the signals received by at least twice as many antennas as used to receive $x(t)$. Similar statements apply to linear combinations involving $x(t)$ and $y_2(t) = x^*(t) e^{+j\pi f_b t}$. Thus, a filtering structure that uses linear combinations of $x(t)$, $y_1(t)$, and $y_2(t)$ can provide the same level of processing gain against other signals, interference, and noise as an appropriate linear combination of the signals received by twice as many antennas as used to receive $x(t)$.

Based on these facts about the spectral correlation properties of GMSK and other MSK signals, two specific interference scenarios are cited here as examples to illustrate, but not to limit, the applicability of the filtering structures of this invention to practical problems in wireless communication systems. For simplicity, only a single antenna is used in both scenarios; the capability of the proposed filtering structures increases proportionately with the number of antennas used.

Co-Channel Interference Rejection

A single antenna receives a desired GMSK signal and a co-channel GMSK signal having the same carrier frequency and comparable signal power. Neither signal can be recovered by a linear time-invariant filtering structure. However, with reference to FIG. 2A through FIG. 2C, each of the components numbered -6 to 0 (and the components in the continuum from -150 kHz to 0 kHz) in $x(t)$ can be linearly combined with its corresponding component in $y_1(t)$ to yield a clean estimate of the desired signal while rejecting the co-channel interference. Similarly, with reference to FIG. 3A through FIG. 3C, each of the components numbered 0 to 6 (and the components in the continuum from 0 to 150 kHz) in $x(t)$ can be linearly combined with its corresponding component in $y_2(t)$ to yield a clean estimate of the desired signal while rejecting the co-channel interference. Thus, by using both $y_1(t)$ and $y_2(t)$, the desired signal can be recovered over the entire band from -150 kHz to $+150$ kHz, or over just the allocated GSM frequency channel from -100 kHz to $+100$ kHz if desired. In this way, the filtering structures in this invention can reject a GMSK co-channel interfering signal even when only one antenna is used.

Adjacent Channel Interference Rejection

A single antenna receives a desired GMSK signal and an adjacent channel GMSK signal possibly having much greater signal power. Specifically, let the adjacent channel signal have a carrier frequency that is 200 kHz (per GSM

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carrier allocation protocols) less than that of the desired signal. Due to spectral leakage, the adjacent channel interferer degrades the desired signal, which cannot be recovered by a linear time-invariant filtering structure. However, with reference to FIG. 3A through FIG. 3C, each of the components numbered 4 to 8 (and the components in the continuum from 100 kHz to 200 kHz) in the adjacent channel interference (now downconverted to zero carrier offset), which comprise the spectral leakage into the desired signal, can be linearly combined with its corresponding component in $y_1(t)$ to cancel out the effects of the spectral leakage. Similarly, an adjacent channel interferer whose carrier is 200 kHz above that of the desired signal can be canceled out. In this way, the filtering structures in this invention can reject two GMSK, adjacent channel interfering signals even when only one antenna is used.

Signal Distortion Correction

When a transmitted signal undergoes propagation through a non-ideal channel, such as a multipath propagation channel, the received signal can be a severely distorted version of the transmitted signal, including for example frequency-selective fading (i.e., deep spectral nulls). However, with reference to FIG. 2A through FIG. 2C, any of the narrowband components numbered -6, -5, ..., -1, 0 (and the components in the continuum from -150 kHz to 0 kHz) in $x(t)$ that have been severely attenuated by frequency-selective fading (to such an extent that they cannot simply be amplified without strongly increasing the noise level), can be replaced with corresponding properly phase-adjusted and amplitude-scaled components in $y_1(t)$, thereby removing the spectral nulls. Similarly, with reference to FIG. 3A through FIG. 3C, spectral nulls in the band from 0 kHz to +150 kHz can be removed.

D. Specific Filtering Structures

Various filtering structures in accordance with the present invention will now be described with reference to FIG. 4 through FIG. 17. In the filtering structures depicted therein, all blocks with only a letter inside denote standard linear time-invariant filters (ignoring time variation due only to adaptation), those blocks with a letter followed by (t) denote linear periodically time-variant filters, and those blocks with a letter and FSE denote linear time-invariant fractionally spaced equalizers (filters), all of which are known in the signal processing art. Furthermore, blocks with the symbol (*) inside denote conjugators, blocks with "Re" inside denote real-part selectors (extractors), and blocks with "Im" inside denote imaginary-part selectors (extractors), each of which operates on a complex signal and each of which is also referred to herein as a polarized switcher. These elements are known to those having ordinary skill in the signal processing art or are as otherwise described herein.

LCL-FRESH Filter

The preceding observations about $|C_{xy}(f)|$ and the role of high or nearly perfect correlation in motivating linear filtering structures are taken into account in the 3-path time-domain linear-conjugate-linear (LCL) frequency-shift filter (FRESH filter) 10 in accordance with the present invention whose structure for GMSK and other MSK signals is depicted in block-diagram form in FIG. 4. When applied to the signal $x(t)$ received at a single antenna, or the other filter structure or the other filter structures described below can separate and/or correct the distortion of two co-channel GMSK or other MSK signals. The 3-path LCL-FRESH filter is appropriate for data that is sampled at a rate higher than

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the bit rate. Accordingly, the output of the 3-path LCL-FRESH filter must be sampled at the bit rate prior to being processed, for example, by a GMSK demodulator that operates on bit-rate sampled data to recover the bit stream.

LCL-PTV Filter

As an alternative to the 3-path LCL-FRESH filter, the effect of the two complementary frequency shifts $e^{j\pi f_b t}$ and $e^{-j\pi f_b t}$ and the associated filters g_{13} and g_c can be achieved also with a single time-variant filter $g(t)$. The resulting structure is referred to herein as a linear-conjugate-linear periodically time-variant (LCL-PTV) filter 12 shown in block-diagram form in FIG. 5. The filter $g(t)$ has a time-variant impulse response according to the possibly periodic time-variations in the possibly cyclostationary statistics of the received signals. In some applications, it is appropriate for the coefficients of time-variant filter $g(t)$ to vary with frequency f_b , in which case there exists a one-to-one mapping between the LCL-PTV filter and the 3-path LCL-FRESH filter. In other applications, such as those in which the time-slot boundaries of the signals do not line up sufficiently well and the length of a time-slot is a non-integer number of bit periods (e.g., as in GSM), the coefficients of filter $g(t)$ must change every time one or more of the received signals encounters a time-slot boundary. The LCL-PTV filter is appropriate for data that is sampled at a rate higher than the bit rate. Accordingly, the output of the LCL-PTV filter must be sampled at the bit rate prior to being processed, for example, by a GMSK demodulator that operates on bit-rate sampled data to recover the bit stream.

LCL-FRESH-FSE Filter

In both the 3-path LCL-FRESH filter and the LCL-PTV filter, the sampling rate of the filter output exceeds the bit-rate (typically for a linear filter processing GMSK, it is an integer multiple of the bit-rate f_b , such as $2f_b$), in which case the filter output is typically sampled at the bit-rate prior to being processed by the demodulator (which provides an estimate of the bit stream conveyed in the desired signal). Thus, an alternative to the 3-path LCL FRESH filter is to move this bit-rate sampling operation back through the filtering structure to the extent possible without changing the capability of the filter and to eliminate redundant operations. Alternative embodiments of the resulting filter structure 14 (type-1) and 16 (type-2), which are referred to herein as LCL-FRESH fractionally-spaced equalizers (LCL-FRESH-FSE), are depicted in block-diagram form in FIG. 6 and FIG. 7, respectively. This filter differs significantly from the LCL time-invariant filter by its particular use of conjugated signal paths and by its special use of frequency shifts to properly exploit the cyclostationarity properties of the GMSK or other MSK signals. The bit-rate-sampled signal estimate $\hat{s}(k)$ must be processed by a demodulator to recover the bit stream.

LCL-FSE and RI-FSE Filters

Straightforward manipulations can be performed on the LCL-FRESH-FSE filter to obtain a filter structure in which the output of each bit-rate-sampler is frequency-shifted by j^{-k} . This shifted signal is then split into two paths: the upper path is processed by an LTI filter, and the lower path is conjugated and processed by another LTI filter. That is, the lower path following each bit-rate-sampler no longer contains a frequency shift of $(-1)^k$. However, the output final summer must be frequency-shifted by j^k . Alternative embodiments of the resulting filter structure 18 (type-1) and

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20 (type2) are shown in FIG. 8 and FIG. 9, respectively. This filter structure will be referred to herein as an LCL-FSE filter rather than an LCL-FRESH-FSE because no path is frequency-shifted any differently than any other path.

The LCL-FSE filter can be simplified without any loss of performance by observing that the two paths at the output of each bit-rate sampler and j^{-k} frequency-shifter form an LCL filter, which can be equivalently realized by modifying the upper path to operate on the real part (instead of operating on the entire complex signal) and by modifying the lower path to operate on the imaginary part (instead of operating on the conjugated complex signal). Since this structure replaces the two LCL filters by two Real-Imaginary (RI) filter pairs, the resulting structure is referred to as the RI-FSE filter and can be interpreted to be an alternate but mathematically equivalent embodiment of the LCL-FSE filter.

Alternative embodiments of this filter structure, 22 (type-1) and 24 (type-2), are shown in FIG. 10 and FIG. 11, respectively.

LCL and RI Filters

The LCL and RI filters are related to the LCL-FSE and RI-FSE filters, but operate on bit-rate-sampled data. This is possible for GMSK, and for some other MSK signals, because the bit-rate is larger than the RF bandwidth occupied by the signal, and so the bit-rate satisfies Nyquist's sampling criterion for this signal.

The result of simplifying the LCL-FSE filter as a result of the input being sampled at the bit-rate is the LCL filter 26, shown in FIG. 12.

The LCL filter can be simplified without any loss of performance by observing that it can be equivalently realized by modifying the upper path to operate on the real part (instead of operating on the entire complex signal) and by modifying the lower path to operate on the imaginary part (instead of operating on the conjugated complex signal). Since this structure replaces the LCL filter by a Real-Imaginary (RI) filter pair, the resulting structure 28, shown in FIG. 13, is referred to as an RI filter and can be interpreted to be an alternate but mathematically equivalent embodiment of the LCL filter.

The RI filter offers the same implementation advantage over the LCL filter as the RI-FSE offers over the LCL-FSE: since the data paths being processed by various LTI or FSE filters are real, both the application of the filters to the data and the adaptation of the filters require less arithmetic than if the data paths were complex. Significantly, since the RI filter is mathematically equivalent to the LCL filter, there is no loss of quality in the estimated signal at the output.

Time-Variant Versions of LCL-FRESH, LCL-FRESH-FSE, LCL-FSE, RI-FSE, LCL, and RI

Although the 3-path LCL-FRESH, LCL-FRESH-FSE, LCL-FSE, RI-FSE, LCL, and RI filters are described herein as if their filter coefficients are fixed over one time-slot, they can in fact be made time-variant (as with the LCL-PTV filter) so as to accommodate the changes in statistical properties of the received signals when, for example, one or more of the signals encounters a time-slot boundary. However, in systems in which there is a high degree of control over the synchronization of the time-slot structures of the multiple co-channel signals, it is advantageous to force the time-slot boundaries of the multiple signals to be as close together as possible. Such desired synchronization

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could certainly arise if the multiple signals originate from mobile units operating in the same cell in a wireless cellular communication system; similarly, if the multiple signals originate from mobile units operating in different cells, then the desired synchronization could be made to arise through cooperation of the base stations in these different cells.

Variations for Improved Rejection of Adjacent Channel Interference

The aforementioned filtering structures are ideally suited for the rejection of co-channel interference and separation of co-channel desired signals and removal of signal distortion. However, variants of these filtering structures can provide rejection of adjacent channel interference by exploiting the spectral correlation properties of the adjacent channel interference in addition to those of the co-channel signals. Capability to reject adjacent channel interference can be especially important when the adjacent channel signals are much stronger than the desired channel signals (e.g., due to the near-far problem). The aforementioned filtering structures can be generalized (or modified) to exploit the cyclostationarity properties of the adjacent channel interference in addition to those of the desired channel signals (or, if desired, only those of the adjacent channel interference). For example, the 3-path LCL-FRESH filtering structure can be generalized to include two additional paths, one for each of the upper and lower adjacent channels that can be present. The signals present in these two additional paths are highly correlated with the interference, from the corresponding adjacent channels, that contaminates the desired signals; thus, correctly linearly combining these additional paths with the other three paths can significantly reduce the adjacent channel interference. The resulting 5-path LCL-FRESH filtering structure 30 is depicted in FIG. 14, wherein $x_{13}(t)$ and $x_{14}(t)$ are the complex envelope signals from the lower and upper adjacent channels (i.e., downconverted to complex base-band from their respective RF carriers), respectively.

Or, if only adjacent channel interference rejection is desired, such that functionality is limited to the cancellation of adjacent channel interference, instead of joint cancellation of adjacent- and co-channel interference, signal separation, and distortion correction, then the two-input two-path LCL-FRESH filtering structures 30a, 30b, which are shown in FIG. 15 (2 I-LCL-FRESH-U) and FIG. 16 (2 I-LCL-FRESH-L), respectively, can be used to remove interference from either the upper adjacent channel by using input $x_{13}(t)$ and frequency shift $f_c - f_d/2$, or the lower adjacent channel by using input $x_{14}(t)$ and frequency shift $-f_c + f_d/2$, respectively, where f_c is the separation between adjacent carriers. As an alternative, interference from both upper and lower adjacent channels can be simultaneously removed using the 3-input 3-path LCL-FRESH filter 30c shown in FIG. 17 (3 I-LCL-FRESH-UL).

Based on the aforementioned descriptions of the manner in which the LCL-PTV, LCL-FRESH-FSE, LCL-FSE, RI-FSE, LCL, and RI filtering structures can be obtained from the 3-path FRESH filter, those skilled in the art of signal processing will appreciate that these filtering structures can also be generalized to improve their ability to reject adjacent channel interference. Such generalization may be accomplished by altering these filtering structures to exploit the cyclostationarity properties of the adjacent channel interference.

Finally, it is noted that combinations of the outputs of various filtering structures may be used advantageously. For

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example, each of two copies of the 3-path LCL-FRESH filter can be applied to each of the lower and upper adjacent channels to provide improved estimates of the adjacent channel signals; these estimates can then be appropriately frequency-shifted, adaptively filtered, and then subtracted from the received data in the desired frequency channel, thus reducing the adjacent channel interference. The signal resulting thereby can then be processed by the 3-path LCL-FRESH filter to separate co-channel signals and correct for distortion. The adjacent channel interference canceling filters can either be adapted jointly with the co-channel separation and distortion correction filters or be adapted separately.

Filter Generalization

It will be appreciated that each of the foregoing filter structures include filters (comprising time-shifters and linear combiners), frequency shifters, polarized switchers (conjugators or real- or imaginary-part selectors), and summers (linear combiners). Thus, common core elements of all of these filtering structures are time-shifters, frequency shifters, polarized switchers, and linear combiners. Table 1 below maps conjugate cycle frequencies and input and output sampling rates into frequency-shift values and polarized switch functions (e.g., conjugation or real- or imaginary-part extraction) defining the specific filter structures described herein.

TABLE 1

Conjugate Cycle Frequencies, Input/Output Sampling Rates†	Filtering Structures*
$\pm f_c/2$, O/O	FIG. 4, LCL-FRESH (FIG. 5, LCL-PTV)
$\pm f_c/2$, O/B	FIG. 7, LCL-FRESH-FSE-2 (FIG. 6, 8, 9, 10, 11)
$\pm f_c/2$, B/B	FIG. 12, LCL (FIG. 13)
$2f_c - f_c/2$, O/O	FIG. 15, 2I-LCL-FRESH-U
$-2f_c + f_c/2$, O/O	FIG. 16, 2I-LCL-FRESH-L
$2f_c - f_c/2$, $-2f_c + f_c/2$, O/O	FIG. 17, 3I-LCL-FRESH-UL
$\pm f_c/2$, $-2f_c + f_c/2$, $2f_c - f_c/2$, O/O	FIG. 14, 5-path LCL-FRESH

† f_c = separation between adjacent carriers, O = oversampled, and B = bit rate sampled.

*The filter structures listed in parenthesis are mathematically equivalent to the basic structure not in parenthesis, and the former can be derived from the latter by standard block diagram manipulations.

It will further be appreciated that each of the foregoing filtering structures are baseband structures designed for complex baseband signals corresponding to a desired down-converted channel centered at zero frequency, and if the desired channel is centered at a non-zero frequency, then standard baseband-to-passband transformations can be applied to these baseband filtering structures to obtain their passband counterparts for processing passband signals.

Joint Space-Time Filtering and Factorized Filtering

As depicted in FIG. 18 and FIG. 19, any of the aforementioned filtering structures can be used in either of two ways: (1) in a joint space-time filtering structure 32, in which each antenna output is processed by one of these filters, and then the filtered outputs are summed, or (2) in a space-time factorized filtering structure 34, in which a purely spatial filter is applied to the received array data, and then the spatially filtered data is processed by one of these filters. The former structure has the capability to attain higher performance at the expense of greater complexity

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than the latter. Alternatively, the 3-path LCL-FRESH filter can be replaced by either the LCL-PTV filter or the 5-path LCL-FRESH filter in either FIG. 18 or FIG. 19, or by any of the LCL-FRESH-FSE, LCL-FSE, RI-FSE, LCL, and RI filters filter in either FIG. 18 or FIG. 19 to yield a bit-rate-sampled estimate $\hat{s}(k)$ of the desired signal waveform.

Variations: Single vs. Multiple Sensors, & Single vs. Multiple Desired Signals

By using the spectral redundancy theory of cyclostationary signals, we have proven mathematically that the best performing method for combining FRESH-filtering capability and spatial-filtering (multi-sensor linear combining) capability is to replicate the best single-sensor FRESH filter by passing the data from each sensor through its own FRESH-filter prior to linearly combining the multiple data sets. This results in a special form of periodically time-variant linear-conjugate spatio-temporal filter. For example, using the single-sensor 3-path LCL-FRESH filter with M sensors yields the 3-path M-sensor LCL-FRESH (3-path MS-LCL-FRESH) spatio-temporal filter. Therefore, it follows that the LCL-FRESH, LCL-PTV, LCL-FRESH-FSE, LCL-FSE, RI-FSE, LCL, and RI filtering structures can apply not only to single-sensor receivers (in which case $x(t)$ is a scalar signal and the LTI or FSE filters in each signal path are single-input filters) but also to multiple-sensor receivers (in which case $x(t)$ is a vector signal and the LTI

or FSE filters in each signal path are multiple-input filters). Similarly, the LCL-FRESH, LCL-PTV, LCL-FRESH-FSE, LCL-FSE, RI-FSE, LCL, and RI LCL filtering structures can be applied to estimate a single desired signal (in which case the LTI or FSE filters in each signal path are single-output filters; in this case, if multiple signals are desired to be estimated, then multiple copies of the filtering structure can be implemented in parallel) as well as to estimate multiple desired signals (in which case the LTI or FSE filters in each signal path are multiple-output filters). It will also be appreciated that the frequency-shifters and conjugators employed in the filters, as well as the real and imaginary part extractors, become multiple-input devices. These observations emphasize the fact that the novelty and utility of the filtering structures reside largely not in the dimensions of the signals in any particular signal path within the structure but rather within the patterns of conjugations, real-part and imaginary-part selections, frequency shifts, and sampling operations and the ways in which linear combinations of signals subjected to these transformations are formed.

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Implementation of LTI and FSE Filters

It will be appreciated by those skilled in the art of signal processing that the linear-time-invariant (LTI) filters and FSE filters describing the various filtering structures of the present invention can be implemented in numerous ways. In one such implementation, an LTI filter is implemented as an FIR filter and is applied to its input by using the well-known overlap-and-add or overlap-and-save algorithms that utilize the fast Fourier transform (FFT). In another such implementation, an LTI filter is implemented using a tapped delay line, multipliers attached to each tap, and a summer. In yet other implementations, the LTI filter might be implemented using an infinite-impulse response (IIR) filter instead of an FIR filter; however, it is noted that adapting such filters can be computationally expensive and/or unreliable.

Demodulation of Recovered Signals

Finally, the interaction between the filtering structures (used for interference rejection and/or co-channel signal separation) and the demodulator (used for recovering the bit stream from the estimate of the desired signal) must be considered. Several demodulators are currently in use for GMSK signals: the maximum likelihood sequence estimator (MLSE) based on the Viterbi algorithm, the multiple-symbol differential detector, and the decision feedback equalizer (DFE). Any one of these demodulators can be used following the filtering structures to demodulate the estimated signal of interest. Alternatively, since the DFE is itself a filtering structure, it is reasonable to merge the demodulation function implemented by the conventional DFE and the interference-rejection and/or signal-separation functions implemented by the space-time filtering structure; the resulting filtering structure would have the desirable property of very low complexity relative to the receivers based on MLSE or multiple-symbol differential detection, and may be capable of attaining a higher-quality recovered bit stream due to its smaller number of adjustable coefficients for the same memory length.

Because all of the alternative filtering apparatus described herein include the capability of the feedforward part of the DFE, the result of merging the filtering apparatus with the DFE is that the filtering apparatus, be it single- or multiple-sensor, is simply followed by the feedback part of the DFE. Thus, the feedback part alone functions as the demodulator. Algorithms for adaptively adjusting this demodulator are not within the scope of this invention.

Similarly, the complexity of the MLSE demodulator can be reduced and its performance can possibly be increased by the effects of the distortion-removal capability of the FRESH filtering structures. For example, the requirements of the channel estimator that is an integral part of the MLSE demodulator can be eased by virtue of the distortion removal or even just distortion reduction.

D. Adaptation Methods

The following discussion of adaptation methods is made in the context of the GSM system, which is one type of wireless communication system to which the present invention applies. Those skilled in the art of communication systems and signal processing will appreciate that these adaptation methods can apply also to any other communication system that uses signals with embedded training sequences and constant modulus.

In GSM, each time slot lasts 577 μ s (156.25 bit periods at 270.833 kbits/sec) and the bits therein are structured as shown in FIG. 20. The three tail bits on either side of the main burst are all zeros, and the guard time lasting 8.25 bit

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periods prevents consecutive time slots from colliding due to different bulk propagation delays. The 116-bit message in each slot can be either digitized speech (after vocoding, convolutional coding, and interleaving) or control information. The 26-bit training sequence embedded in the middle of the slot is used in existing conventional GSM receivers, for example, to perform channel estimation prior to MLSE demodulation.

In the present invention, not only is this convenient training sequence exploited but also the very useful constant-envelope (or constant-modulus) property of GMSK and other MSK signals is exploited to adapt the filter coefficients. Four adaptation algorithms are explained, the first two of which are conventional and the second two of which are novel and unique to the present invention:

1. conventionally trained adaptive equalization;
2. constant-modulus (CM) blind adaptive equalization;
3. training-augmented constant-modulus (TACM) partially blind adaptive equalization, and
4. training-constrained constant-modulus (TCCM) partially blind adaptive equalization.

The first two adaptation algorithms provide the building blocks for the second two. All four of these algorithms are directed to finding the values of the filter coefficients in a matrix W that controls a linear combiner (the number of columns in this matrix is set to be equal to the number of desired signals to be estimated and the number of rows is set to be equal to the number of weights in the linear combiner to be adaptively adjusted). The linear combiner applies these complex weights to the elements of a vector $y(t)$ containing various differently delayed, frequency-shifted, and possibly conjugated versions of the received data from the M -sensors (denoted by the vector $x(t)$) to produce a vector of signal estimates $\hat{s}(t) = W^H y(t)$, where superscript H denotes conjugate transposition. For example, in the 3-path LCL-FRESH filter, let each of the three filters be implemented by a tapped delay line (TDL) containing L taps, followed by a linear combiner. Then the signal estimate $\hat{s}(t)$ can be expressed as

$$\hat{s}(t) = W^H y(t), \quad (4)$$

$$y(t) = \begin{bmatrix} x(t) \\ x(t-1) \\ \vdots \\ x(t-L+1) \\ x^*(t)e^{j\omega_d t} \\ x^*(t-1)e^{j\omega_d t} \\ \vdots \\ x^*(t-L+1)e^{j\omega_d t} \\ x^*(t)e^{-j\omega_d t} \\ x^*(t-1)e^{-j\omega_d t} \\ \vdots \\ x^*(t-L+1)e^{-j\omega_d t} \end{bmatrix}$$

where $y(t)$ is defined in the obvious way. Similar definitions of $y(t)$ can be constructed for the LCL-FRESH-FSE, LCL-FSE, RI-FSE, LCL, and RI filtering structures, as well as for the 5-path LCL-FRESH filter and any extensions of these in which the number of paths, patterns of conjugations or real-part and imaginary-part selectors, choices of frequency shifts, and origins of input signals for the various paths are

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altered. Thus, in the following descriptions, no further reference to the specific dependencies of $y(t)$ on $x(t)$ is made, since all of the filtering structures of the present invention can be represented using this common framework. Furthermore, to simplify the descriptions only the case of W having a single column is considered; to emphasize that the matrix degenerates to a vector in this case, the set of filter coefficients is denoted by w .

Conventionally Trained Adaptive Equalization

In conventionally trained adaptive equalization, the receiver knows that a training signal $s_0(t)$ is transmitted by the desired user starting at time t_{01} and ending at time t_{02} . This knowledge and the received data are used to find w . Specifically, w is chosen so as to minimize the time-averaged squared error between the training signal $s_0(t)$ and the estimate $\hat{s}(t)$ over the training interval (i.e., for all $t \in T_0$, where $T_0 = [t_{01}, t_{02}]$):

$$\min_w \left\langle |s(t) - \hat{s}(t)|^2 \right\rangle_{t \in T_0} \rightarrow w = (R_{yy}^{(T_0)})^{-1} R_{ys_0}^{(T_0)} \quad (5)$$

where $\langle \cdot \rangle_{t \in T_0}$ denotes time-averaging over the period $t_{01} \leq t \leq t_{02}$, and the correlation matrix between any two vectors (or scalars) $a(t)$ and $b(t)$ is defined by $R_{ab}^{(T_0)} = \langle a(t)b^H(t) \rangle_{t \in T_0}$. With reference to GSM systems and FIG. 21, T_0 is the time interval containing the embedded training signal. This method is simple to implement, with low computational complexity when implemented using the recursive least squares (RLS) algorithm or any of the various fast algorithms (e.g., the modular multichannel method or the fast affine projection algorithm), and it converges reliably and quickly when the length of the training sequence exceeds the number of filter coefficients (i.e., the length of w). However, in the presence of strong co-channel interference and/or when the length of w is comparable to or greater than the length of the training signal, this conventionally trained adaptive equalization algorithm may not provide sufficiently reliable or high quality signal estimates.

Constant Modulus (CM) Blind Adaptive Equalization

In total contrast to the conventionally trained adaptive equalizer, the constant-modulus (CM) blind adaptive equalizer ignores completely any training signal information. Instead, in this application it exploits the fact that GMSK and other MSK signals have constant modulus (constant envelope), a property which is degraded or destroyed by the presence of co-channel interference and channel distortion. The CM algorithm attempts to solve the following minimization problem:

$$\min_w \left\langle \left| \hat{s}(t) - \frac{\hat{s}(t)}{|s(t)|} \right|^2 \right\rangle_{t \in T_1} \quad (6)$$

where T_1 our case is the set of all time instants within a particular time slot. Since this is a nonlinear minimization problem with no closed-form solution, and a least-mean-squares (LMS) stochastic gradient descent algorithm would converge much too slowly, an iterative block approach (the so-called least-squares CM algorithm) is used. At the k^{th} iteration, the weight vector W_k is found by solving

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$$\min_{w_k} \left\langle |w_k^H y(t) - z_{k-1}(t)|^2 \right\rangle_{t \in T_1} \quad (7)$$

where

$$z_{k-1}(t) = \frac{w_{k-1}^H y(t)}{|w_{k-1}^H y(t)|}.$$

That is, the modulus normalized term in the squared error is obtained from the filter output during the previous iteration. This allows a simple linear solution to be found:

$$W_k = (R_{yy}^{(T_1)})^{-1} R_{yz_{k-1}}^{(T_1)}. \quad (8)$$

These two steps, finding the modulus-normalized filter output and solving this linear equation, are iterated until convergence. The CM algorithm allows the equalizer to be adapted successfully in the absence of a training signal, although it is known to exhibit unreliable convergence in some situations and its reliability may be lower than desired when the observation interval is short (as it is for GSM time slots). Another complication with the CM algorithm arises when it is applied to environments containing multiple co-channel signals. Multi-target CM algorithms have been developed to allow multiple sets of filter weights to be adapted, each to extract a different signal. A subsequent signal sorting operation must then be performed to identify the correspondence between extracted signals and users; this correspondence can be achieved easily if the users are assigned different training sequences. Notice though that this training signal information might be more profitably used for adaptation of the filter weights themselves rather than simply to sort the output of the blindly adapted filters. This observation is pursued through two other adaptive algorithms, discussed below.

Training-Augmented CM (TACM) Partially Blind Adaptive Equalization

Unlike the conventionally trained adaptive equalizer and the CM blind adaptive equalizer, each of which ignores significantly useful information that is exploited by the other, the TACM partially blind adaptive equalizer exploits both is the training signal available in GSM and the constant modulus property. The motivation for these approaches is the belief that minimization of a convex linear combination of the two cost functions in Equation (5) and Equation (7) will yield better performance than minimization of either cost function alone, since the two cost functions cannot be simultaneously minimized in general. The resulting cost function is given by

$$\gamma \langle |w_k^H y(t) - z_{k-1}(t)|^2 \rangle_{t \in T_1} + (1-\gamma) \langle |w_k^H y(t) - s_0(t)|^2 \rangle_{t \in T_0} \quad (9)$$

As in the conventionally training adaptive equalizer for use in GSM systems, T_0 is the time interval occupied by the embedded training signal (FIG. 20). The time period T_1 over which the CM algorithm is applied can be either the entire time slot, or the time slot excluding the training signal. Either way, the closed-form solution is given by

$$W_k = [\gamma R_{yy}^{(T_1)} + (1-\gamma) R_{yy}^{(T_0)}]^{-1} [\gamma R_{yz_{k-1}}^{(T_1)} + (1-\gamma) R_{ys_0}^{(T_0)}]. \quad (10)$$

The convex linear combiner weight γ controls the influence of the two cost functions and thus also controls the emphasis given to the two distinct types of knowledge used in them.

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Values of γ closer to unity give more influence to the exploitation of the CM property, whereas values closer to zero give more influence to the exploitation of the known training signal.

It is noted that a special case of the TACM arises when the initial condition is zero (i.e., $w=0$) and $\gamma=1$. In this case, the TACM yields an algorithm in which the known training signal is used to initialize the conventional CM algorithm at iteration $k=1$ with $w_1=[R_{yy}^{(T_0)}]^{-1}R_{ys_0}^{(T_0)}$.

In the event that $s_0(t)$ is a constant-modulus signal (e.g., as it is for GMSK, CPFSK, and other MSK signals), then $s_0(t)$ should be pre-normalized to have unity modulus prior to its use in the TACM algorithm. In the event that $s_0(t)$ is not a constant-modulus signal, then an adaptive normalization constant must be inserted into the TACM cost function above to yield the adaptively normalized TACM (AN-TACM) algorithm:

$$\gamma \langle |w_k^H y(t) - z_{k-1}(t)|^2 \rangle_{t \in T_1} + (1-\gamma) \langle |w_k^H y(t) - c^* s_0(t)|^2 \rangle_{t \in T_0} \quad (11)$$

where c is a complex scalar. The solution to this problem is obtained by equating to zero the complex gradients with respect to w_k^* and c^* to obtain

$$\{ \gamma R_{yy}^{(T_1)} + (1-\gamma) R_{yy}^{(T_0)} \} w_k - (1-\gamma) R_{ys_0}^{(T_0)} c = \gamma R_{ys_{k-1}}^{(T_1)} \quad (12)$$

$$R_{s_0 s_0}^{(T_0)} w_k - R_{s_0 c}^{(T_0)} c = 0. \quad (13)$$

This system of equations is solved by

$$\begin{bmatrix} w_k \\ c \end{bmatrix} = \begin{bmatrix} R_{11} & R_{12} \\ R_{21} & R_{22} \end{bmatrix}^{-1} \begin{bmatrix} \gamma R_{ys_{k-1}}^{(T_1)} \\ 0 \end{bmatrix} \quad (14)$$

where $R_{11} = \gamma R_{yy}^{(T_1)} + (1-\gamma) R_{yy}^{(T_0)}$, $R_{12} = -(1-\gamma) R_{ys_0}^{(T_0)}$, $R_{21} = R_{s_0 y}^{(T_0)}$, and $R_{22} = -R_{s_0 c}^{(T_0)}$.

Training-Constrained CM (TCCM) Partially Blind Adaptive Equalization

The TCCM differs from the TACM in the way that it attempts to exploit both types of knowledge. In particular, when the number of filter coefficients is greater than or equal to the number of samples of the training signal, the linear system used by the conventionally trained adaptive equalizer (implied in Equation (5)) is under-determined. That is, the training signal is sufficient only to restrict w_k to lie in some subspace but not to define it completely. Specifically, in this case all that can be said so far is that w_k is given by

$$w_k = (R_{yy}^{(T_0)})^+ R_{ys_0}^{(T_0)} + v_k, \quad v_k \in \mathcal{N}(R_{yy}^{(T_0)}) \quad (15)$$

where superscript $(\cdot)^+$ denotes the Moore-Penrose pseudoinverse, v_k is any vector in the null space of $R_{yy}^{(T_0)}$, and $w^{(T_0)}$ is defined in the obvious way. The constraint on v_k ensures that $w_k^H y(t)$ exactly reproduces the training interval signal over time interval T_0 . The CM property is then used to select v_k , where k denotes as before the iteration in the block least-squares implementation of the CM algorithm. The resulting cost function is obtained simply by substituting Equation (15) into Equation (7) and minimizing with respect to v_k to obtain the solution

$$v_k = V_n (V_n^H R_{yy}^{(T_0)} V_n)^{-1} V_n^H R_{ys_{k-1}}^{(T_0)}, \quad (16)$$

where

$$\mu_{k-1}(t) = z_{k-1}(t) - (w^{(T_0)})^H y(t) \quad (17)$$

and the columns of V_n are the eigenvectors associated with the negligible eigenvalues of $R_{yy}^{(T_0)}$.

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An alternative algorithm can be obtained by first solving for the CM weight vector according to Equation (8) and then projecting this vector onto the affine space $\{w_k = w^{(T_0)} + V_n c_k; \text{ for all } c_k\}$. The resulting solution is

$$w_k = w^{(T_0)} + V_n V_n^H (R_{yy}^{(T_0)})^{-1} R_{ys_{k-1}}^{(T_0)} \quad (18)$$

and thus $w_k^H y(t)$ exactly reproduces the training signal over the time interval T_0 . As with the TACM, if the desired signal $s_0(t)$ has constant modulus, as is the case with GMSK, CPFSK, and other MSK signals, then $s_0(t)$ should be normalized to have unity modulus. Finally, each of the two versions of the TCCM algorithm may be extended, through the adaptive normalization technique used in deriving the AN-TACM algorithm, to accommodate desired signals having non-constant modulus. These extensions are straightforward to obtain and so are not elaborated upon here.

Additional Adaptation Methods

In addition to the aforementioned TACM and TCCM methods and their various extensions and special cases, other adaptation methods can also be used to select the values of the coefficients in the filter. For example, the so-called decision-direction method of adaptation, in which a first phase of adaptation proceeds under control of some method (e.g., any of the aforementioned methods) to produce a medium-to-high quality signal estimate which can then be demodulated, and the resulting detected bits are used to form a known training signal which can be used together with the original perfect training data in a second phase of adaptation (e.g., through the conventional least-squares method).

E. Preferred Embodiment of an Overall Receiver

FIG. 22 depicts a preferred embodiment of an overall receiver apparatus 36 in accordance with the present invention. RF signals at the outputs of the M antennas 38 are coherently downconverted to complex baseband and digitized by a Coherent M -Antenna Collection Apparatus 40, the output of which is a digitized discrete-time sequence of $M \times 1$ complex vectors, denoted elsewhere herein as $x(t)$. This vector of received data is then provided as input to a Filter Apparatus 42, which takes as its other input the filter coefficients (W) provided by an Adaptation Apparatus 44, to produce a digitized discrete-time sequence of $d \times 1$ complex vector signal estimates $\hat{s}_k(n)$. During the adaptation process, a switch 46 at the output of the Filter Apparatus 42 directs the output of the Filter Apparatus 42 into the Adaptation Apparatus 44; upon the detection of convergence by the Adaptation Apparatus 44, such as when a prescribed number of iterations are completed, the switch 46 is moved to direct the output of the Filter Apparatus 42, the final signal estimate $\hat{s}(n)$, to subsequent processors such as demodulators that will render bit decisions to recover the binary data streams conveyed by the desired signals. An intermediate output of the Filter Apparatus 42 (denoted by $y(t)$ herein) is also provided as input to the Adaptation Apparatus 44.

A preferred embodiment of the Coherent M -Antenna Collection Apparatus 40 is shown in FIG. 23. This apparatus is controlled by user-supplied values for the RF tuning frequency and the A/D sampling clock frequency to control the local oscillators 48 used for the downconversion from real RF to complex baseband (also known as in-phase and quadrature-phase signals) and for the A/D converters 50, respectively. The RF front-ends 52 that perform this down-conversion can be implemented in a variety of ways known well to those skilled in the art of RF design. Preferably the sampling clock frequency of the A/D converters is equal to

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an integer multiple of the bit rate f_b . With the exception of the LCL and RI filters for which a sampling rate off f_b is needed, $2f_b$ represents a good compromise between the temporal resolution provided by oversampling and the excess of data and associated computational load induced by oversampling. The digitized discrete-time inphase and quadrature signals at the output of each pair of A/D converters are combined to form a representation of a single complex-valued digitized discrete-time signal. The M signals so formed, one per chain of hardware associated with a single antenna, are grouped into a digitized discrete-time $M \times 1$ complex vector of received data.

A preferred embodiment of the Filtering Apparatus 42 is shown in FIG. 24. The Filtering Apparatus 42 accepts as input the $M \times 1$ complex vector of received data and the filter coefficients output from the Adaptation Apparatus 44. The received data is first processed by a Frequency-Shifting, Time-Shifting, and Polarized Switching (i.e., conjugation, real-part selection or imaginary-part selection) Apparatus 54 whose purpose is to form the vector $y(t)$. The record of such transformed data $y(t)$ is stored in a Signal Buffer 56, which can be implemented using dynamic RAM chips or other high-speed storage media, to allow $y(t)$ to be made available to the Adaptation Apparatus 44 over multiple iterations of the adaptation algorithm implemented therein. A Matrix-Vector Multiplier 58 applies the filter coefficients to the received data, producing output $\hat{s}_k(n)$ for use in the subsequent iteration (number $k+1$) performed by the Adaptation Apparatus 44.

A preferred embodiment of the Frequency-Shifting, Time-Shifting, and Polarized Switching Apparatus 54 is shown in FIG. 25 for the particular filtering structure corresponding to the 3-path LCL-FRESH filter described previously herein. It will be appreciated that the other filters previously described herein could be alternatively used. The apparatus accepts as an input the $M \times 1$ vector $x(t)$ of received data and produces as an output the $3LM \times 1$ vector $y(t)$ according to Equation (4). The Tapped Delay Lines (TDL) 60 produce sets of L time-shifted versions of their respective inputs. The Polarized Switcher 62 which, in this embodiment is a conjugator, and the conjugator 62 and Frequency-Shifters 64 are depicted using standard block symbols from signal processing and communications. Similarly, the preferred embodiments of the Frequency-Shifting, Time-Shifting, and Polarized Switching Apparatus 54 for the particular filtering structures corresponding to the LCL-FRESH-FSE (2 alternate embodiments), the LCL-FSE (2 alternate embodiments), the RI-FSE (a second alternate embodiment can be obtained for the RI-FSE, similar to the LCL-FRESH-FSE and LCL-FSE), the LCL, and the RI filters are shown in FIG. 26 through FIG. 32, respectively, in which the intermediate signal vector at the output is $4LM \times 1$ or $2LM \times 1$. Note that the embedded training sequences that appear in the desired signal(s) must also be bit-rate sampled by a Bit-Rate Sampler 66 and frequency-shifted by j^{-k} prior to using them in adaptation methods applied to the LCL-FSE, RI-FSE, LCL, and RI filters; it is similarly noted that this frequency shift j^{-k} must be compensated for at the filter output by a conjugate frequency shift j^{-k} if it is desired to obtain an unshifted estimate of the desired signal. However, many GMSK demodulators require their inputs to be shifted by j^{-k} anyway, and so the conjugate frequency shift j^k and the subsequent frequency shift j^{-k} cancel each other. It is also noted that the RI-FSE and RI filters can be more efficiently implemented due to their use of real-valued intermediate signal vectors (and consequent arithmetic simplification of filter application and adaptation), instead of the

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complex-valued intermediate signal vectors that are used in the LCL-FSE and LCL filters.

Referring to FIG. 33, Polarized Switcher 62 is depicted in a functional block diagram form. It can be seen at this point that Polarized Switcher 62 represents a family of filter design parameters wherein the real 68 and imaginary 70 parts of the complex input signal are optionally selected or deselected by selectors 72 according to the conjugate cycle frequencies and input/output sampling rates shown in Table 1. Note also that the sign of the imaginary part of the input signal, if selected, is either unaffected or flipped by switch 74. In other words, the complex input signal is either conjugated, its real part is selected, or its imaginary part is selected. It will be appreciated, however, that Polarized Switcher 62 is preferably implemented as a single function conjugator or real-part selector or imaginary-part selector rather than an apparatus that provides for the alternative selection of each of those functions.

Referring to FIG. 34, a preferred embodiment of the Tapped Delay Line (TDL) 60 is shown. This is a standard implementation of what is known in the art. The $LM \times 1$ output vector $b(t)$ produced thereby is defined simply for any $M \times 1$ input vector $a(t)$ to be

$$b(t) = \begin{bmatrix} a(t) \\ a(t-1) \\ \vdots \\ a(t-L+1) \end{bmatrix} \quad (19)$$

Thus, TDL 60 can accommodate either a real input or a complex input, with appropriate modification to the underlying unit-sample delay devices and signal paths.

Referring now to FIG. 35 and FIG. 36, alternative embodiments of Adaptation Apparatus 44 are shown where TACM and TCCM are the chosen adaptation algorithms, respectively, and all of the d desired signals in the $d \times 1$ vector $s_0(t)$ have constant unit modulus. Adaptation Apparatus 44 takes as inputs the intermediate signal data $y(t)$, provided by the signal buffer 56 in the Filtering Apparatus 42, and the output signal $\hat{s}_k(t)$ computed by the Matrix-Vector Multiplier 58 in the Filtering Apparatus 42. It provides as output the matrix W_k of filter coefficients, and it also controls the output switch 46 shown in FIG. 22. Adaptation Apparatus 44 further moves the switch 46 from its lower position, in which Filtering Apparatus 42 output is directed to the Adaptation Apparatus 44, to its upper position, in which the Filtering Apparatus 42 output is the $d \times 1$ vector $\hat{s}(t)$ of desired signal estimates, suitable for subsequent use by an appropriate demodulator. It should be understood by those skilled in the art of implementing (e.g., on programmable digital signal processing chips) that alternate embodiments of the Adaptation Apparatus 44 follow directly from the descriptions of the various versions of TACM and TCCM in this document.

As can be seen, the present invention can be used to extract a signal of interest from a plurality of spectrally overlapping communications signals, separate and remove distortion from interfering co-channel signals and suppress adjacent-channel interfering signals of the GMSK or MSK type. These signals characteristically have real and imaginary components, exhibit spectral and temporal overlap, exhibit temporal redundancy, have conjugate cycle frequencies equal to twice their carrier frequencies plus and minus one-half of their data bit rate, and exhibit conjugate spectral redundancy for spectral components having frequencies

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separated by their conjugate cycle frequencies. The present invention provides for time-shifting, frequency-shifting, and conjugation or extraction of real or imaginary parts of complex signals, to produce an estimate of the signal of interest. While the description above contains many specificities, these should not be construed as limiting the scope of the invention but as merely providing illustrations of some of the presently preferred embodiments of this invention. Thus the scope of this invention should be determined by the appended claims and their legal equivalents.

We claim:

1. An apparatus for extracting a signal of interest from a plurality of spectrally and temporally overlapping input signals containing digital data having a bit rate, said input signals having carrier frequencies, said input signals having conjugate cycle frequencies equal to twice their carrier frequencies plus and minus one-half of their data bit rate, said input signals exhibiting conjugate spectral redundancy for spectral components having frequencies separated by said conjugate cycle frequencies, said input signals exhibiting temporal redundancy, said apparatus comprising:

- (a) time-shifting means for producing a time-shifted output signal wherein said signal of interest is time-shifted;
- (b) frequency-shifting means for producing a frequency-shifted output signal wherein said signal of interest is frequency-shifted by an amount determined by its cycle frequencies; and
- (c) linear combining means for weighting and summing said output signals to produce an estimate of said signal of interest.

2. An apparatus as recited in claim 1, wherein said input signals having real and imaginary components, and further comprising polarized switching means for producing a polarized output signal wherein said real and/or imaginary components of said signal of interest are selected or deselected and wherein said real and imaginary components have signs which are changed or unchanged by said polarized switching means, wherein said polarized output signal is time-shifted, and wherein said polarized output signal is weighted and summed by said linear combining means.

3. An apparatus as recited in claim 1, further comprising receiving means for receiving said plurality of spectrally and temporally overlapping input signals.

4. An apparatus as recited in claim 1, further comprising demodulator means for extracting data from said estimate of said signal of interest.

5. An apparatus for extracting a signal of interest from a plurality of spectrally and temporally overlapping communications signals, said communications signals having real and imaginary components, said communications signals having carrier frequencies, said communications signals containing digital data having a bit rate, said communications signals exhibiting temporal redundancy, said communications signals having conjugate cycle frequencies equal to twice their carrier frequencies plus and minus one-half of their data bit rate, said communications signals exhibiting conjugate spectral redundancy for spectral components having frequencies separated by said conjugate cycle frequencies, said apparatus comprising:

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- (a) time-shifting means for producing a time-shifted output signal wherein said signal of interest is time-shifted;
- (b) polarized switching means for producing a polarized output signal wherein said real and/or imaginary components of said signal of interest are selected or deselected and wherein said real and imaginary components have signs which are changed or unchanged by said polarized switching means;
- (c) frequency-shifting means for producing a frequency-shifted output signal wherein said signal of interest is frequency-shifted by an amount determined by its cycle frequencies; and
- (d) linear combining means for weighting and summing said output signals to produce an estimate of said signal of interest.

6. An apparatus as recited in claim 5, further comprising receiving means for receiving said plurality of spectrally and temporally overlapping communications signals.

7. An apparatus as recited in claim 6, further comprising demodulator means for extracting data from said estimate of said signal of interest.

8. An apparatus for extracting a signal of interest from a plurality of spectrally and temporally overlapping communications signals containing digital data having a bit rate, said communications signals having carrier frequencies, said communications signals having conjugate cycle frequencies equal to twice their carrier frequencies plus and minus one-half of their data bit rate, said communications signals exhibiting conjugate spectral redundancy for spectral components having frequencies separated by said conjugate cycle frequencies, said communications signals exhibiting temporal redundancy, said apparatus comprising:

- (a) sensor means for receiving said communications signals;
- (b) filter means for frequency-shifting, time-shifting and polarized switching of said signal of interest contained in said communications signals, wherein said signal of interest is frequency-shifted by an amount determined by its cycle frequencies;
- (c) means for adapting said filter means; and
- (d) means for producing an estimate of said signal of interest.

9. An apparatus as recited in claim 8, wherein said sensor means comprises an antenna and a radio frequency receiver.

10. An apparatus as recited in claim 9, further comprising signal buffering means for buffering an output signal from said filter means and producing and intermediate signal as an input to said adapter means.

11. An apparatus as recited in claim 10, further comprising matrix-vector multiplier means, coupled to said signal buffer means and said adapter means, for weighting and linearly combining signals from said filter means.

12. An apparatus as recited in claim 11, further comprising demodulator means for extracting digital data contained in said estimate of said signal of interest.

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